

FIG. 1

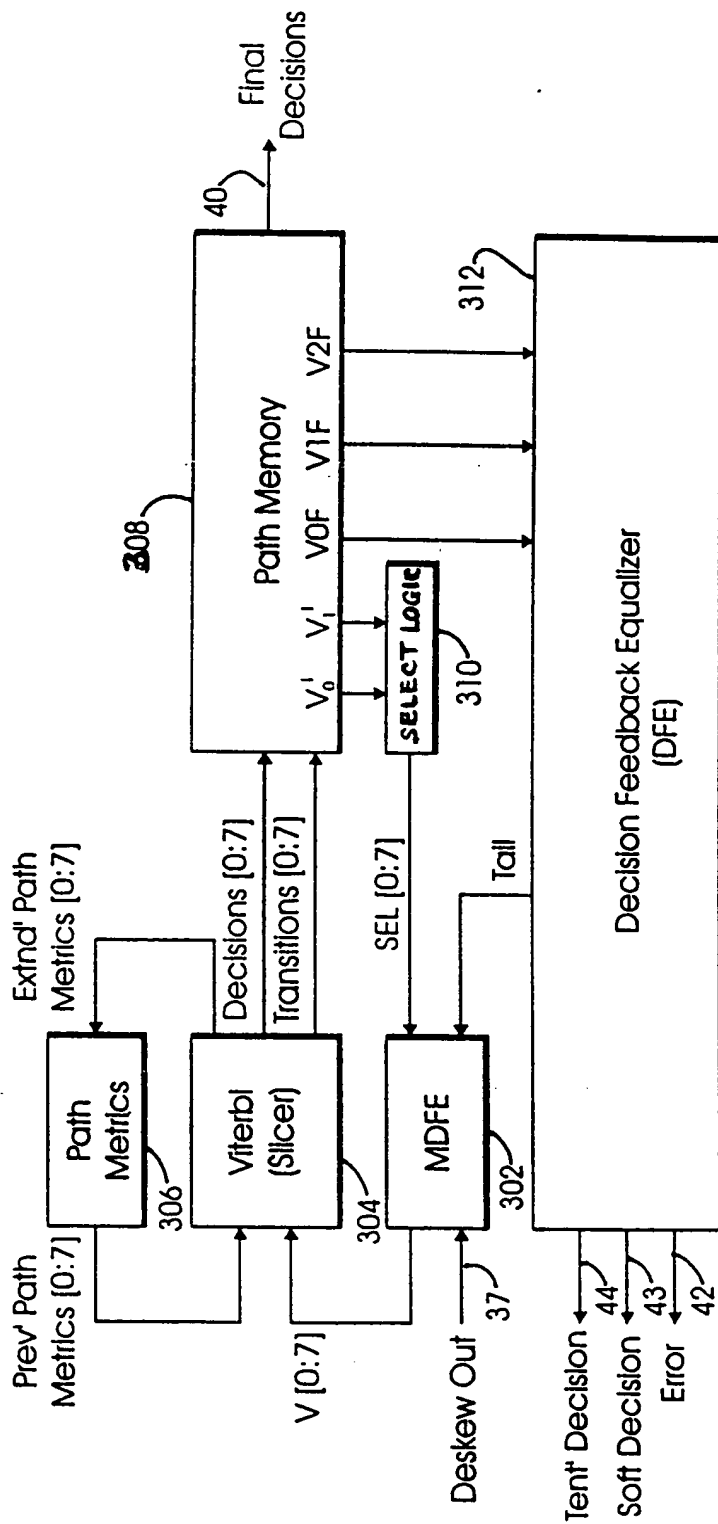


FIG. 3

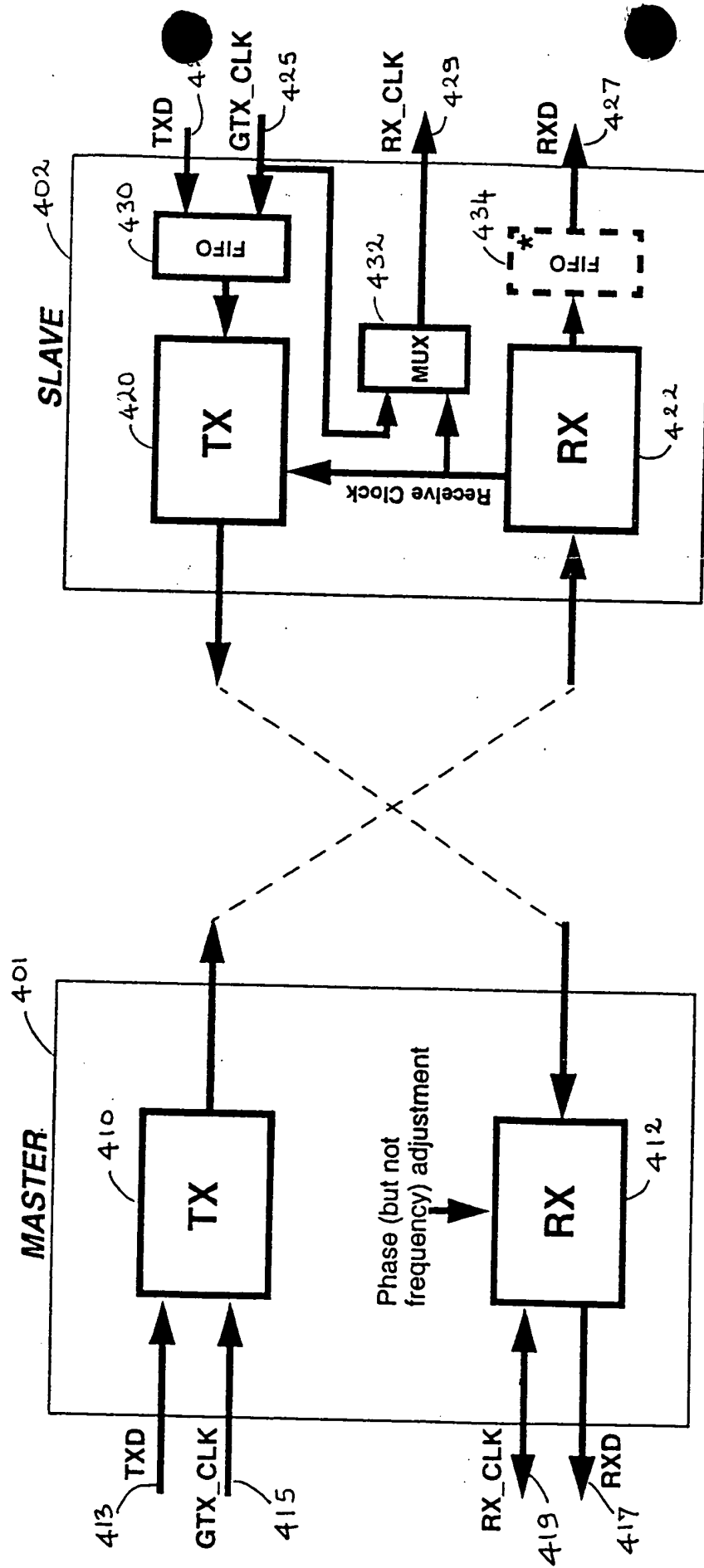


FIG. 4

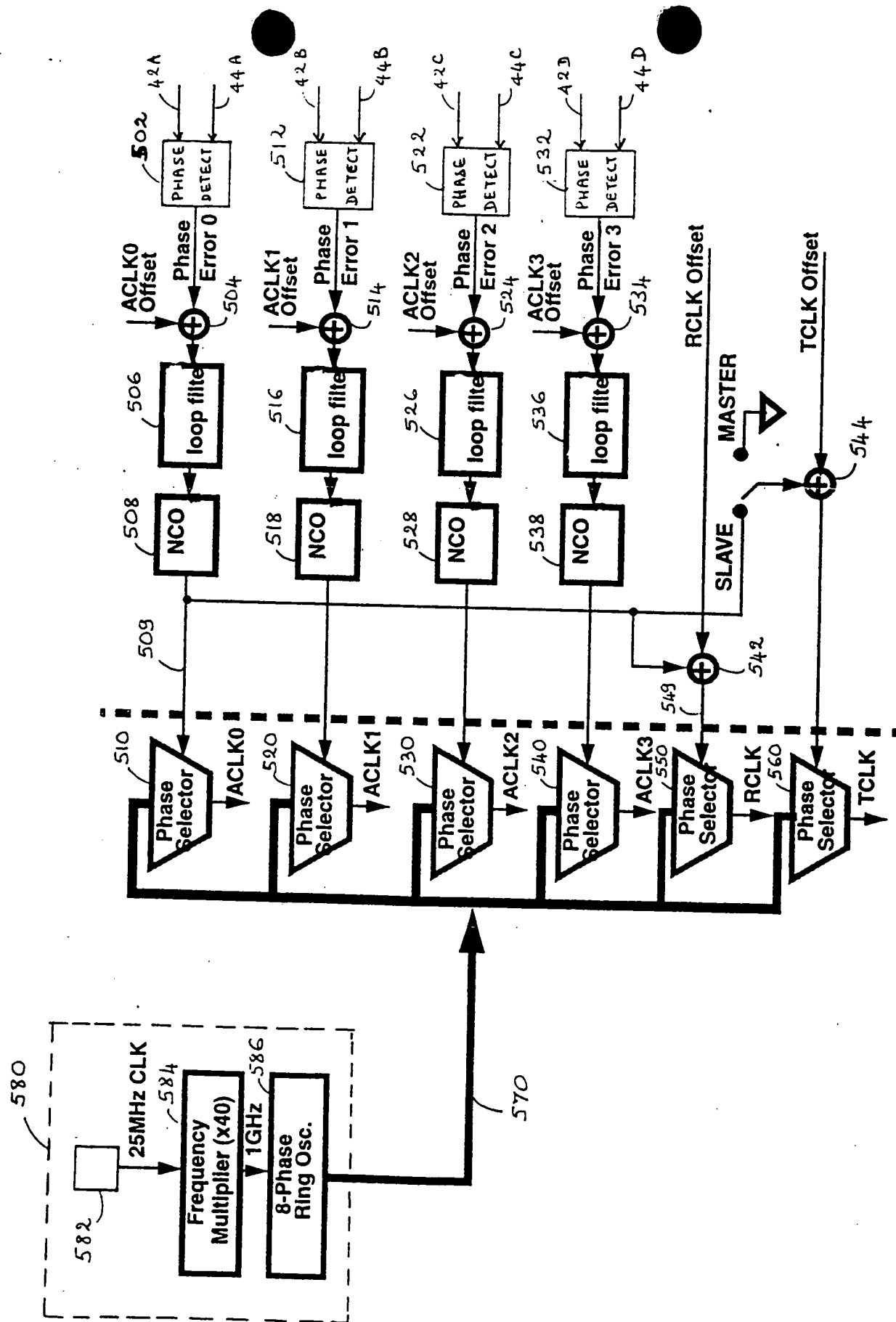


FIG. 5

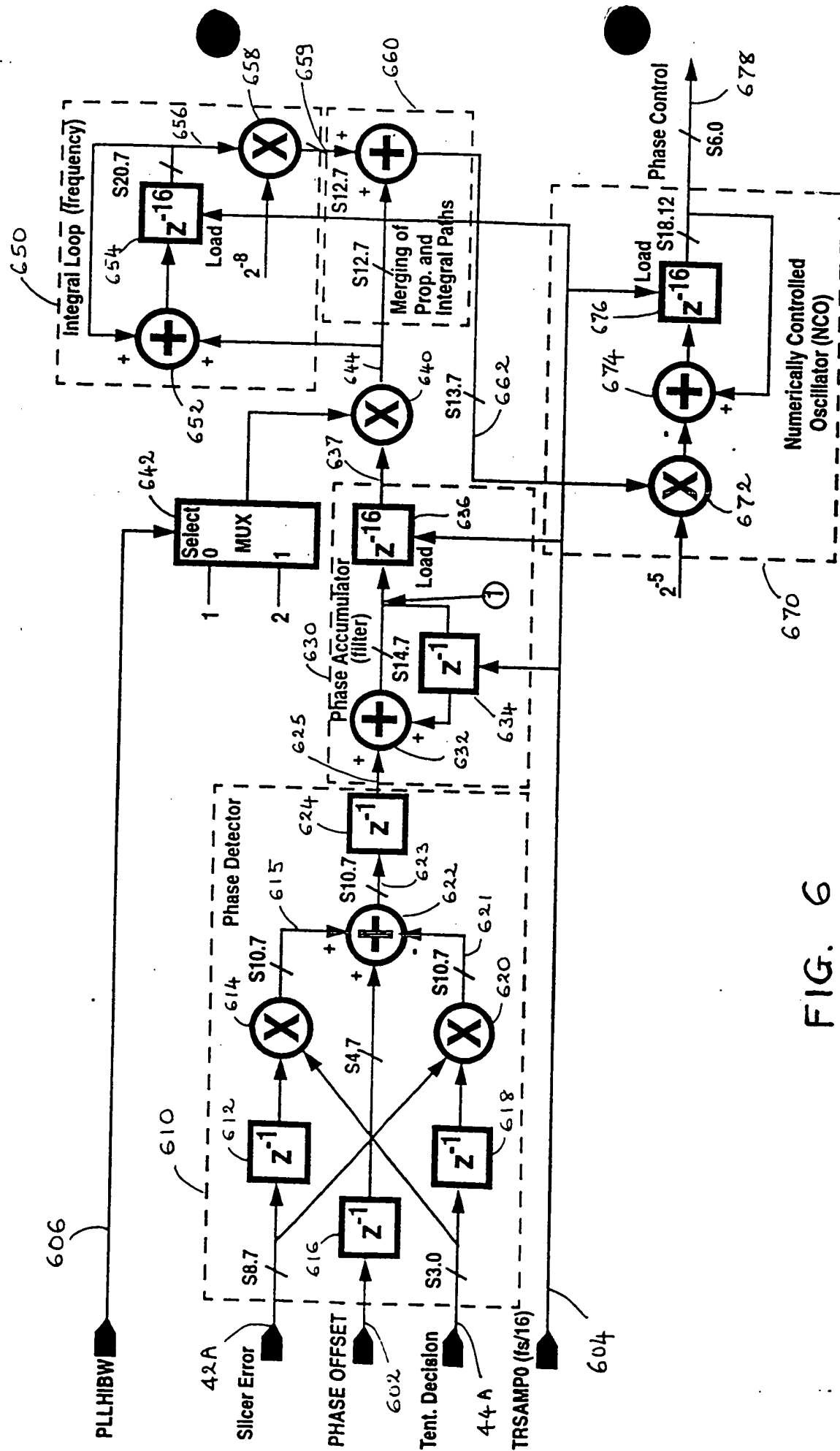


FIG. 6

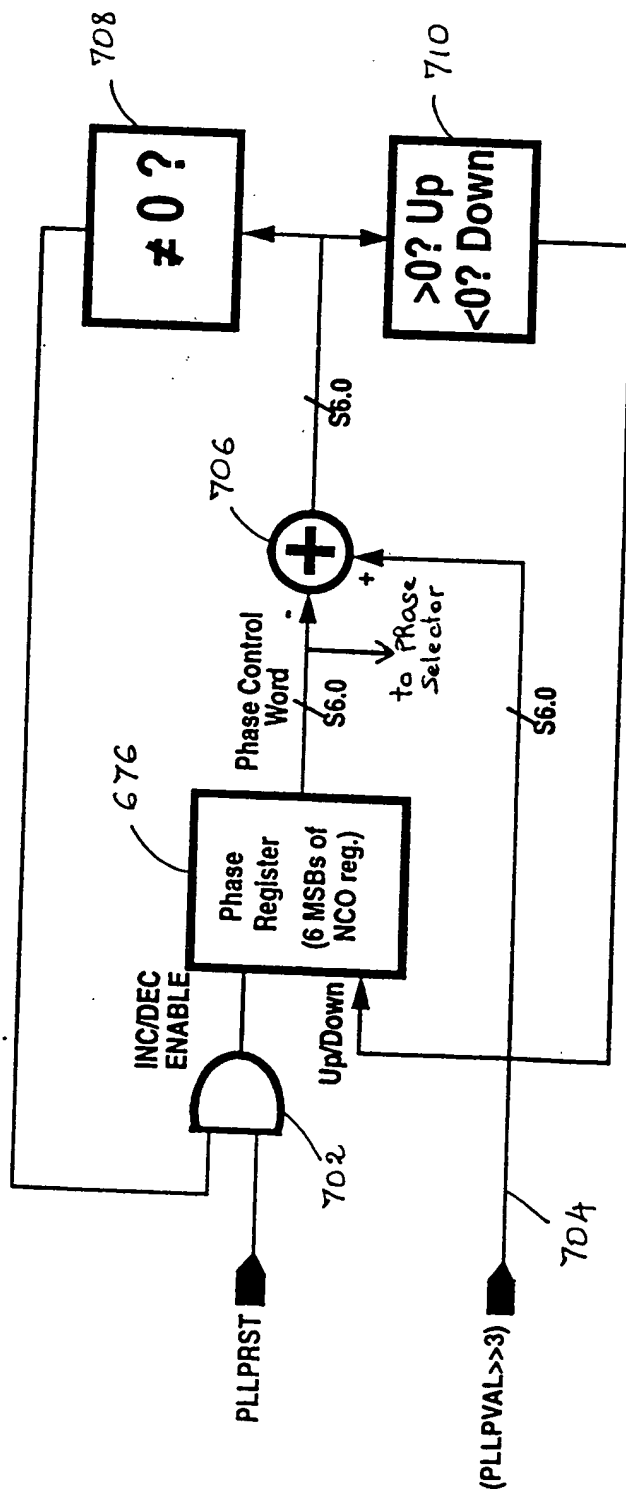


FIG. 7

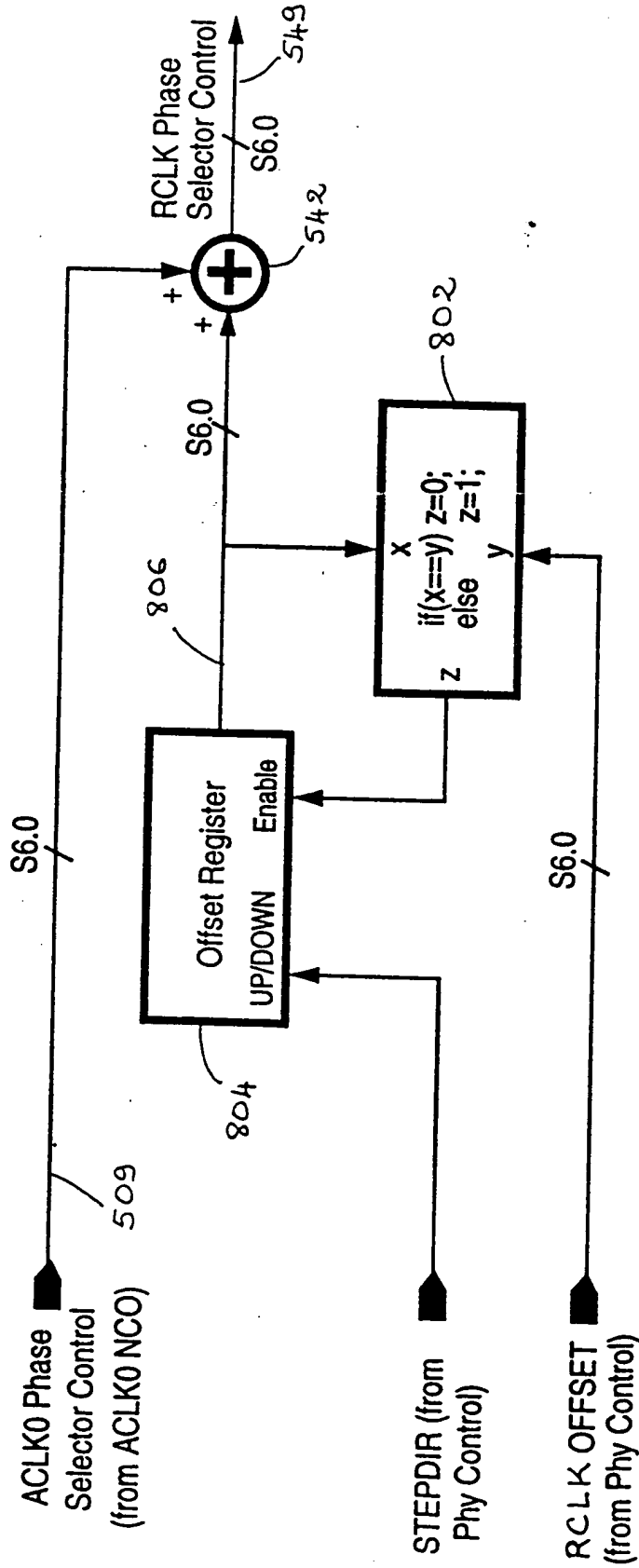


FIG. 8

004240" 422/5560

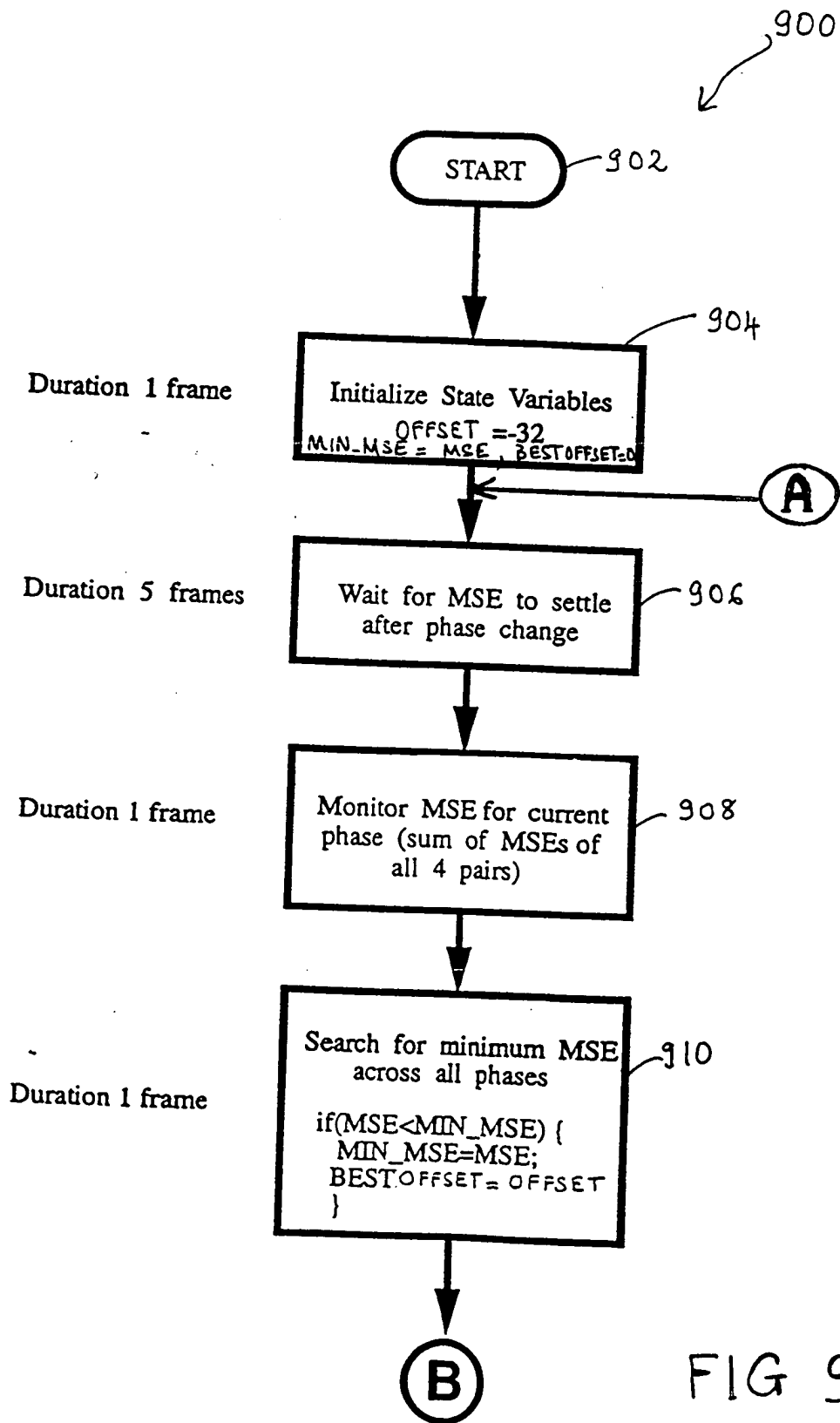


FIG 9 (1 of 2)

004240-422550

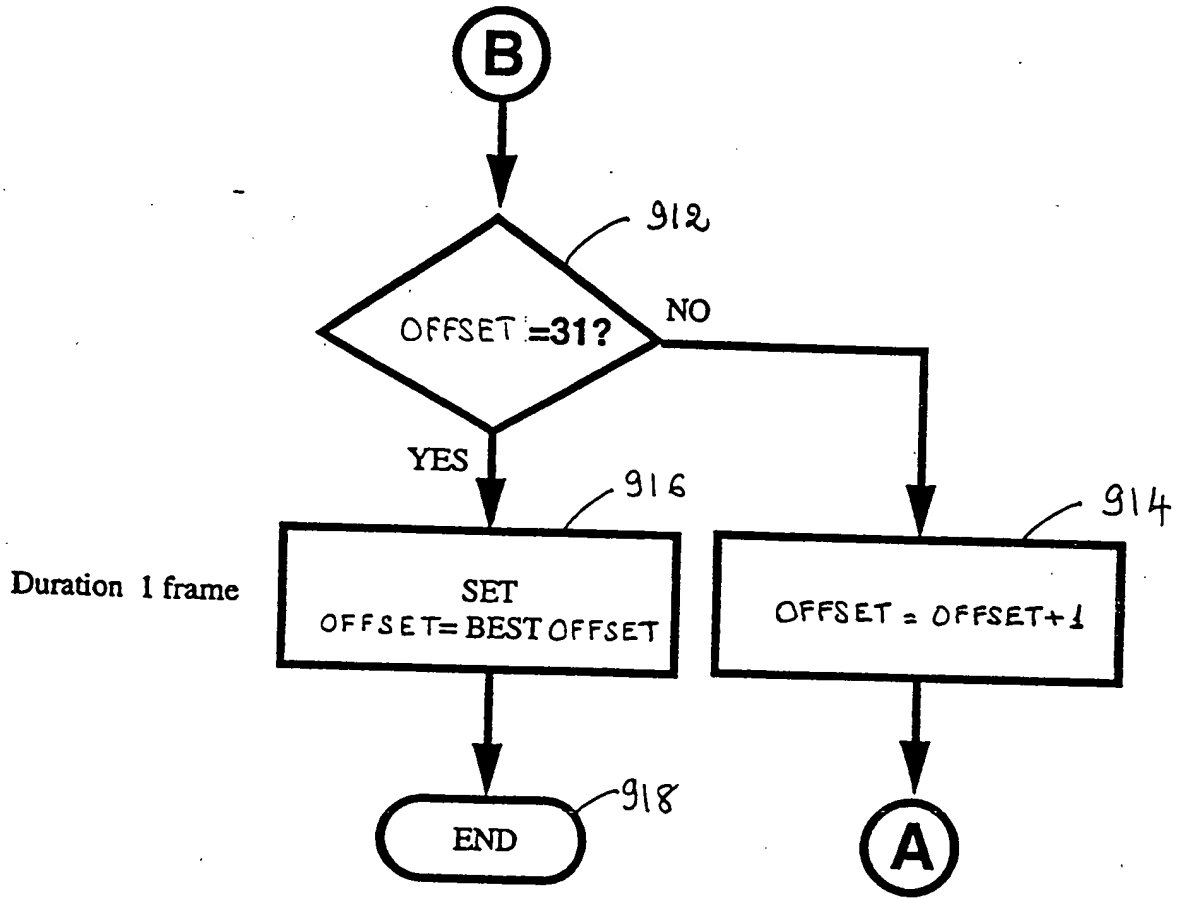
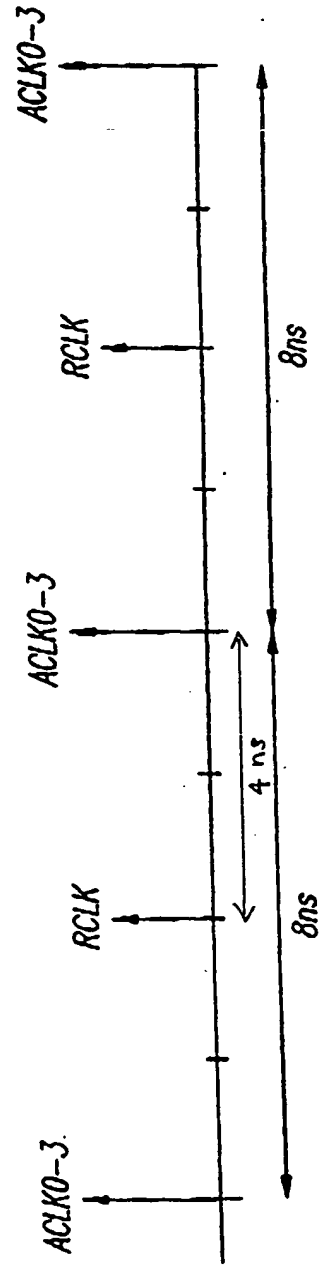
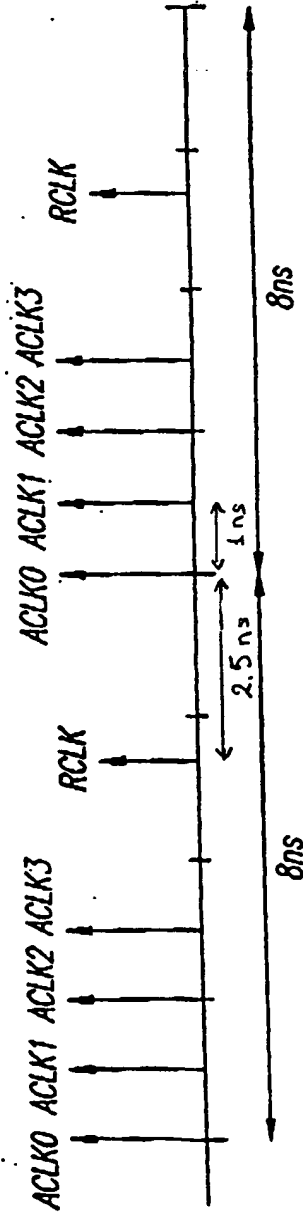
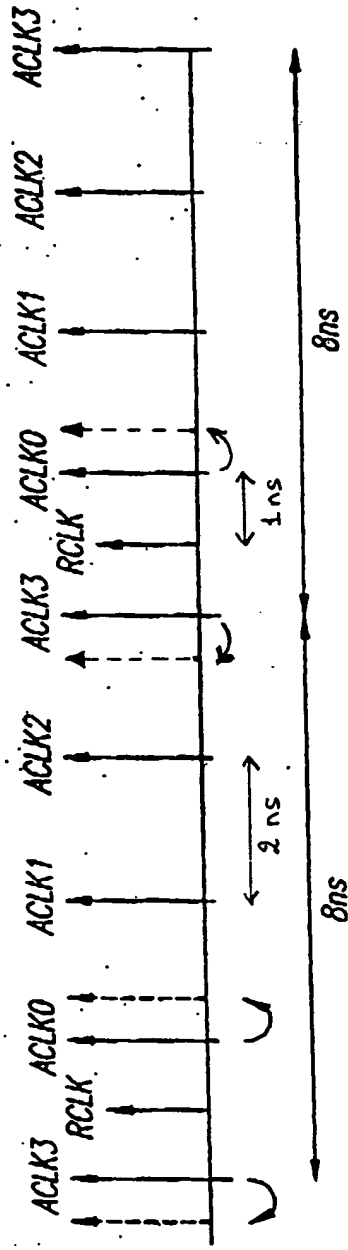


FIG. 9 (2 of 2)



004240" 422560

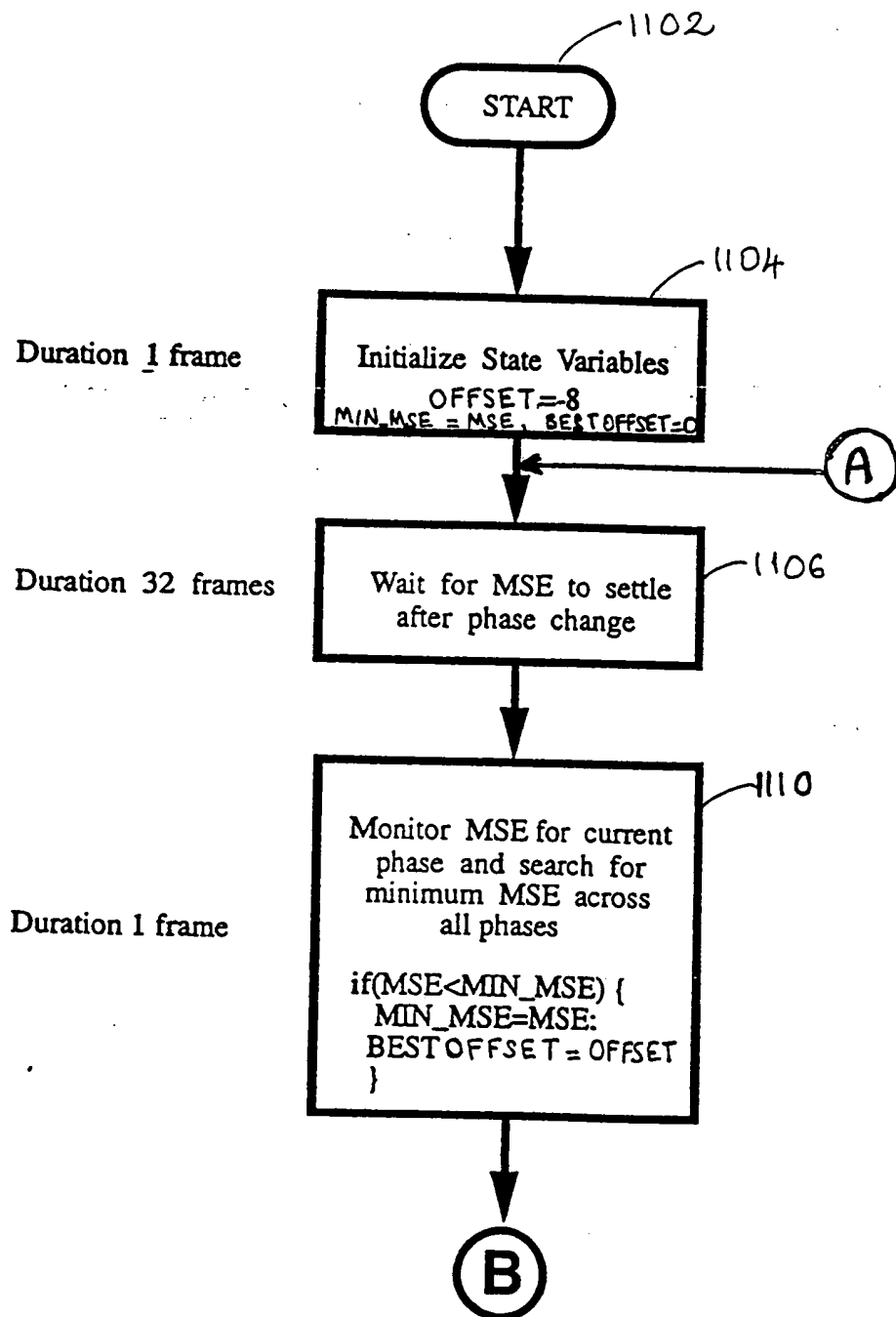


FIG. 11 (1 of 2)

004240" 422550

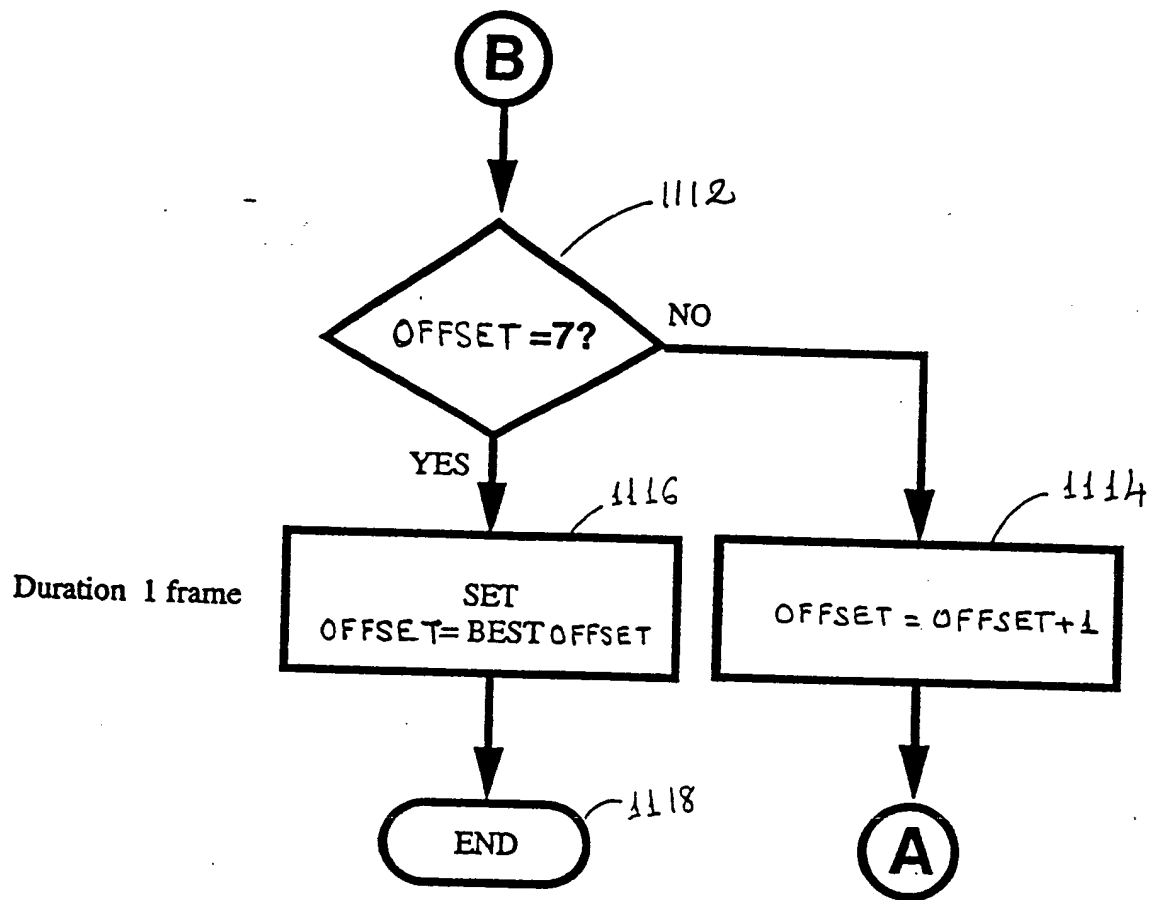


FIG. 11 (2 of 2)

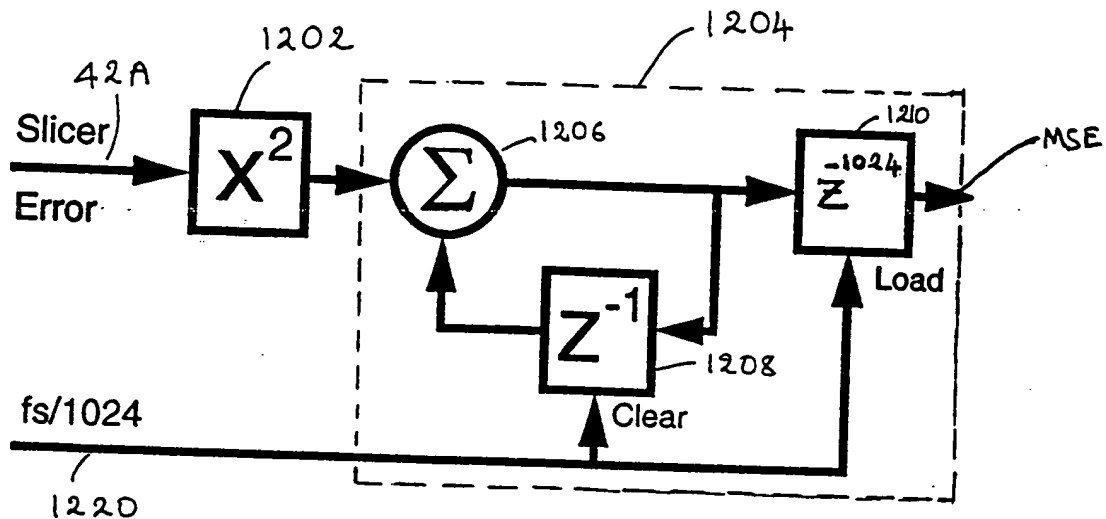


FIG. 12

004240" 422550

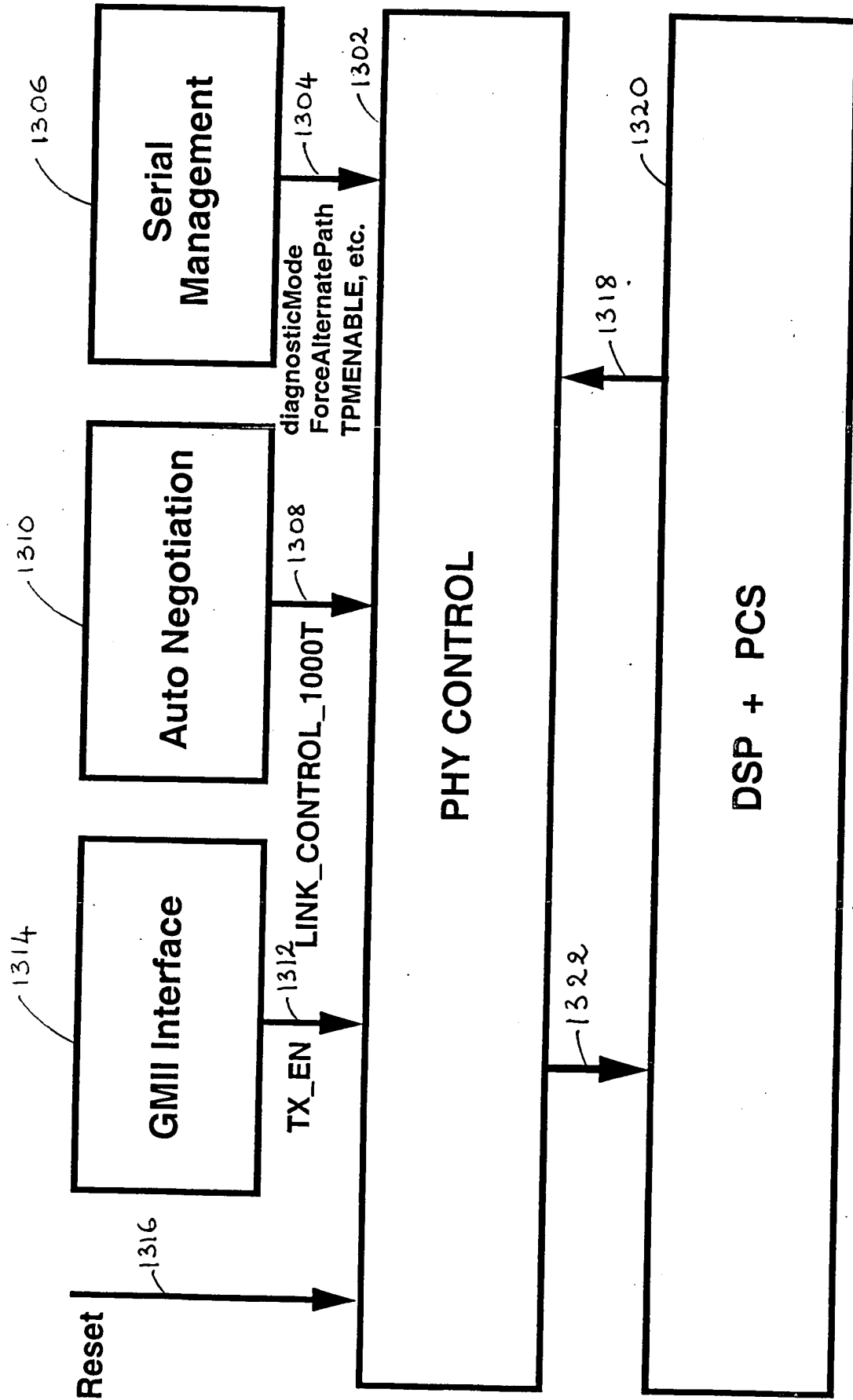


FIG. 13

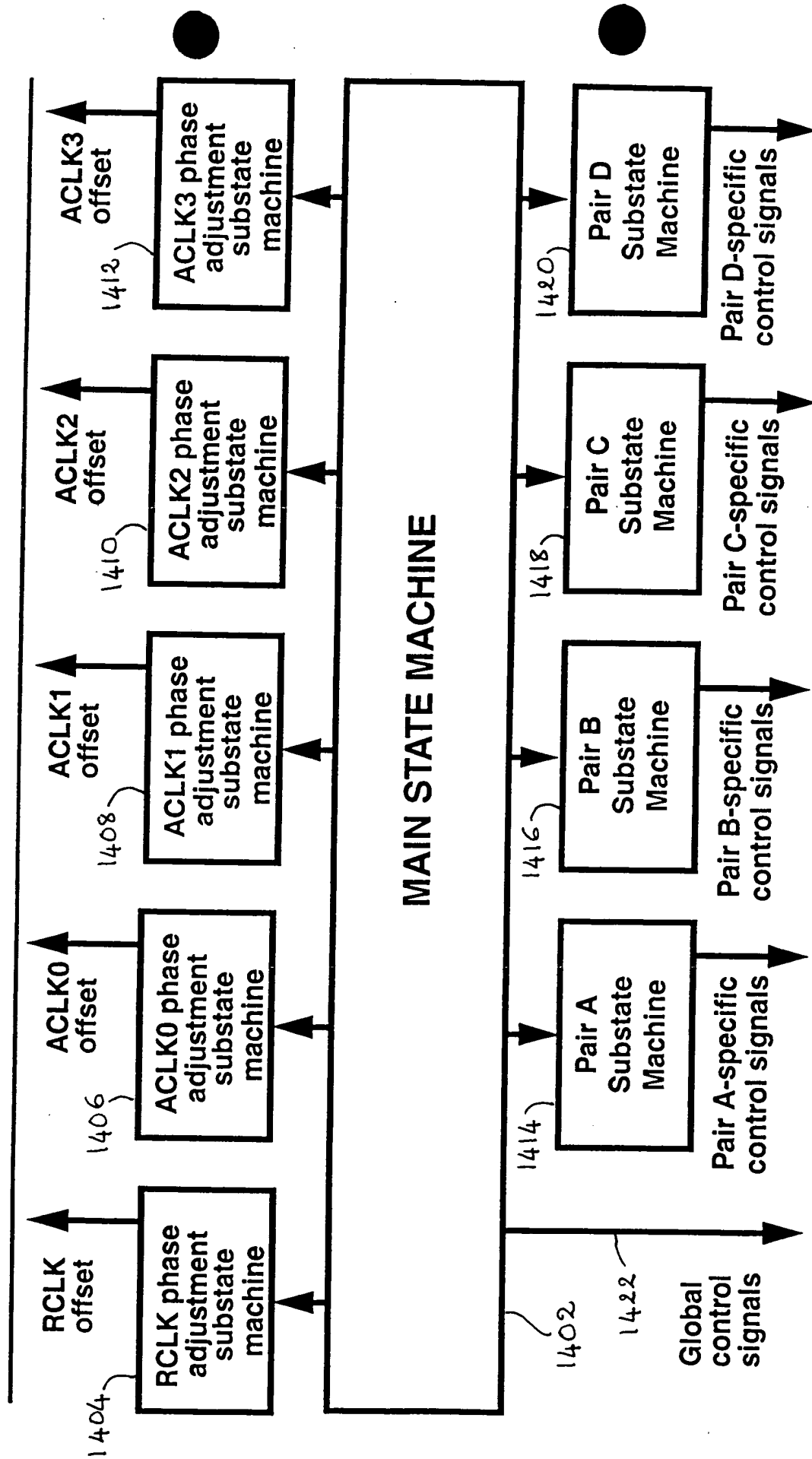
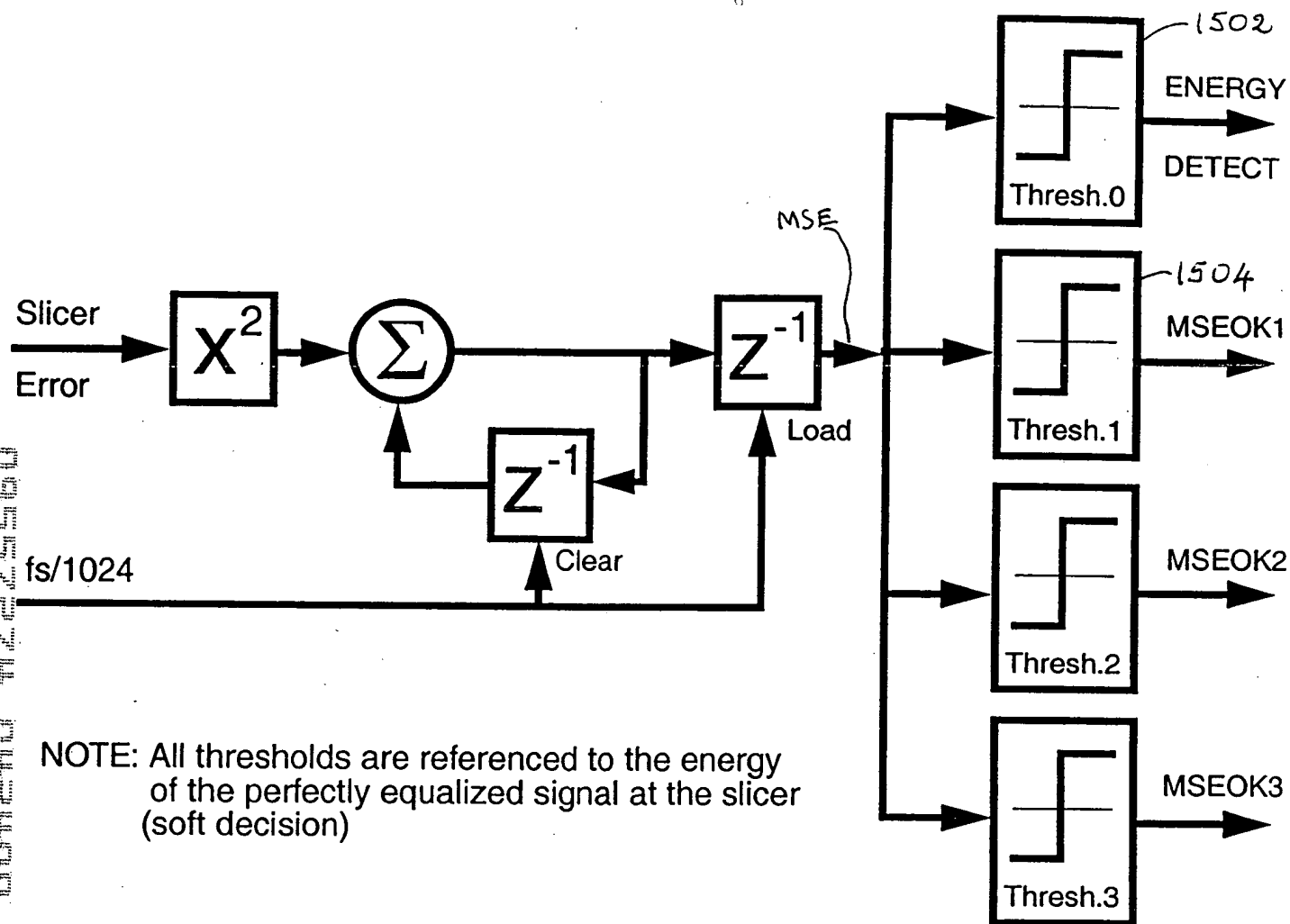


FIG. 14

001240"422550



| THRESHOLD | VALUE[dB] | FUNCTION |
|--------------|-------------------|-----------------------------|
| Thresh.0 | -20 | ENERGY DETECT |
| Thresh.1 | -17 | loc_rcvr_status_OK |
| Thresh.2 | -12 | frequency recovery |
| Thresh.3 (1) | -22.8 (SER=1e-13) | Quality Indicator |
| Thresh.3 (2) | -22.5 (SER=1e-12) | Quality Indicator |
| Thresh.3 (3) | -22.0 (SER=1e-11) | Quality Indicator (default) |
| Thresh.3 (4) | -21.5 (SER=1e-10) | Quality Indicator |

FIG. 15

001240" 422550

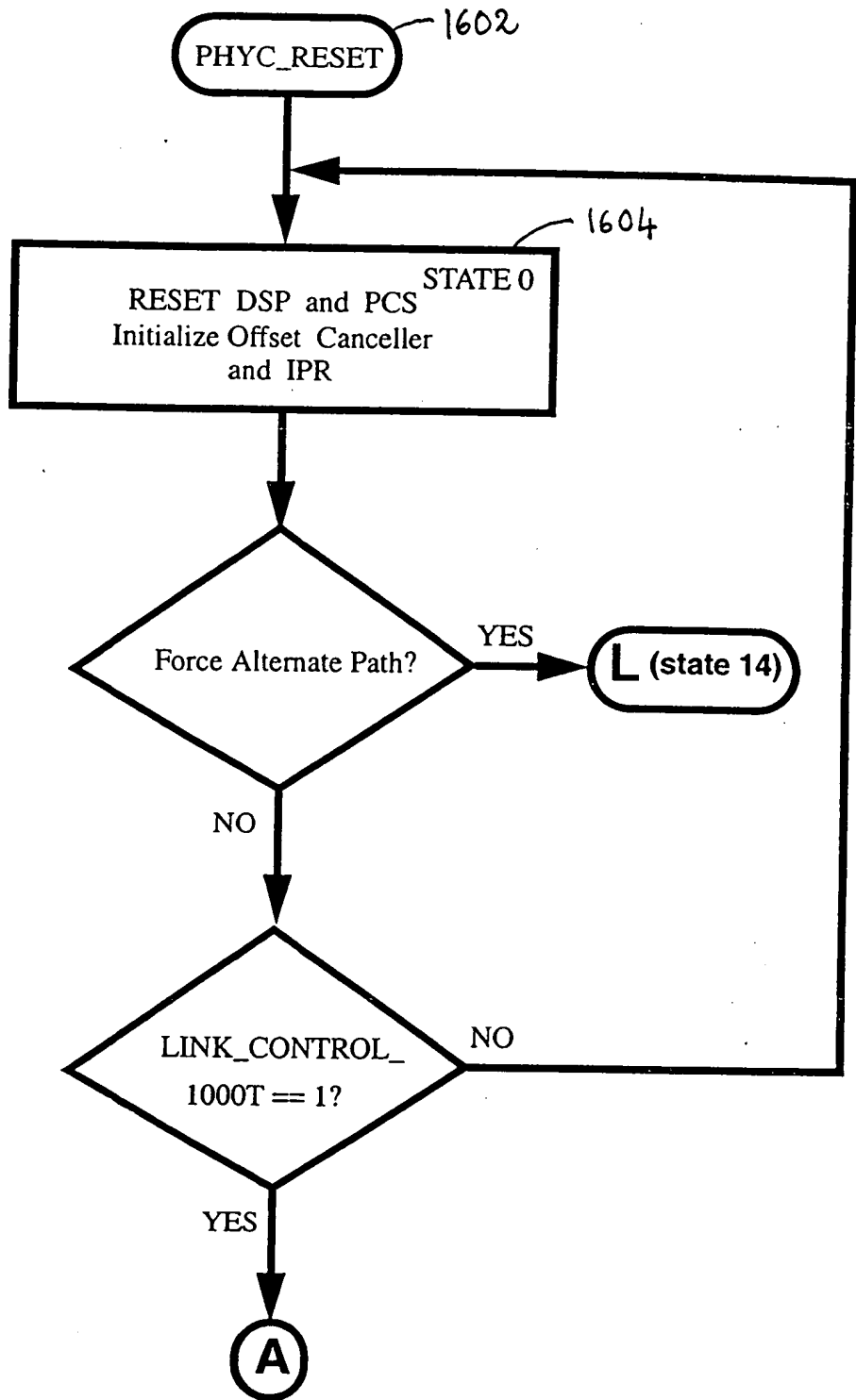


FIG. 16A

004240"422550

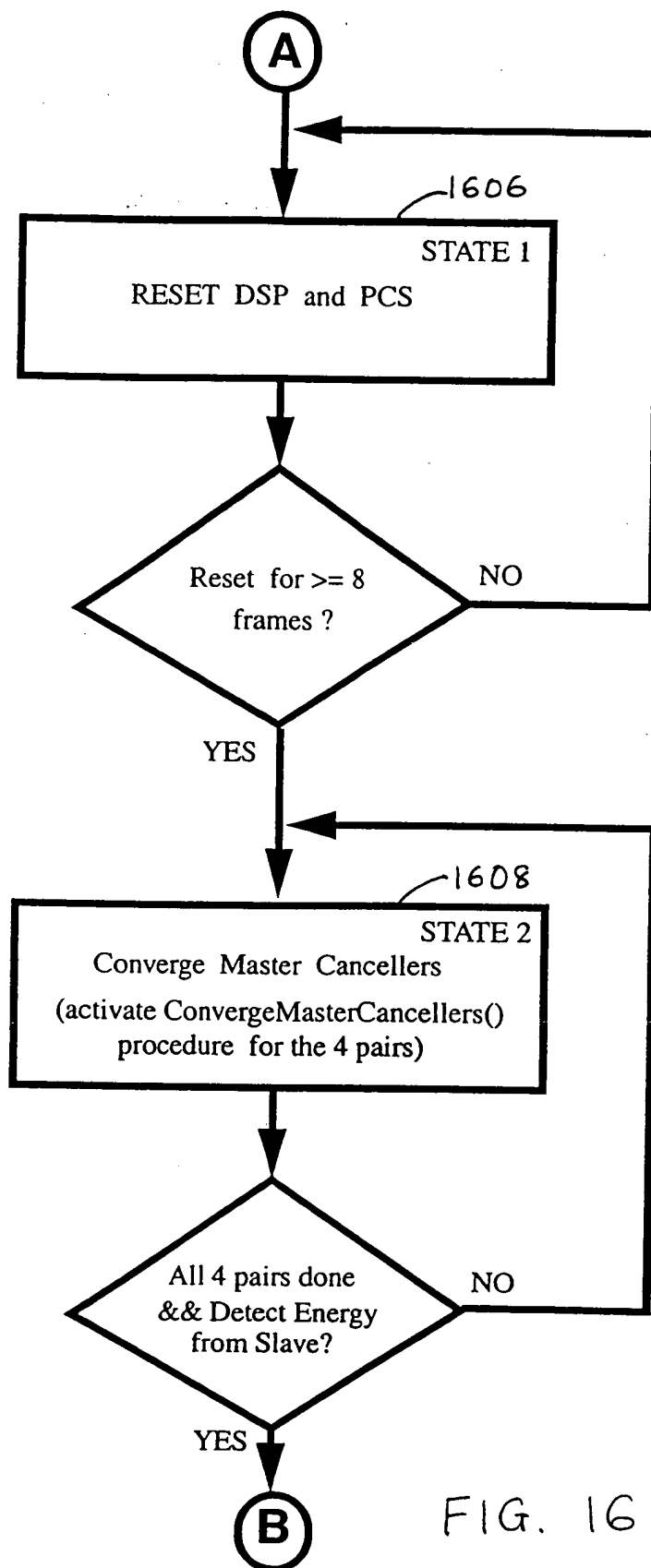


FIG. 16 B

004240" 422550

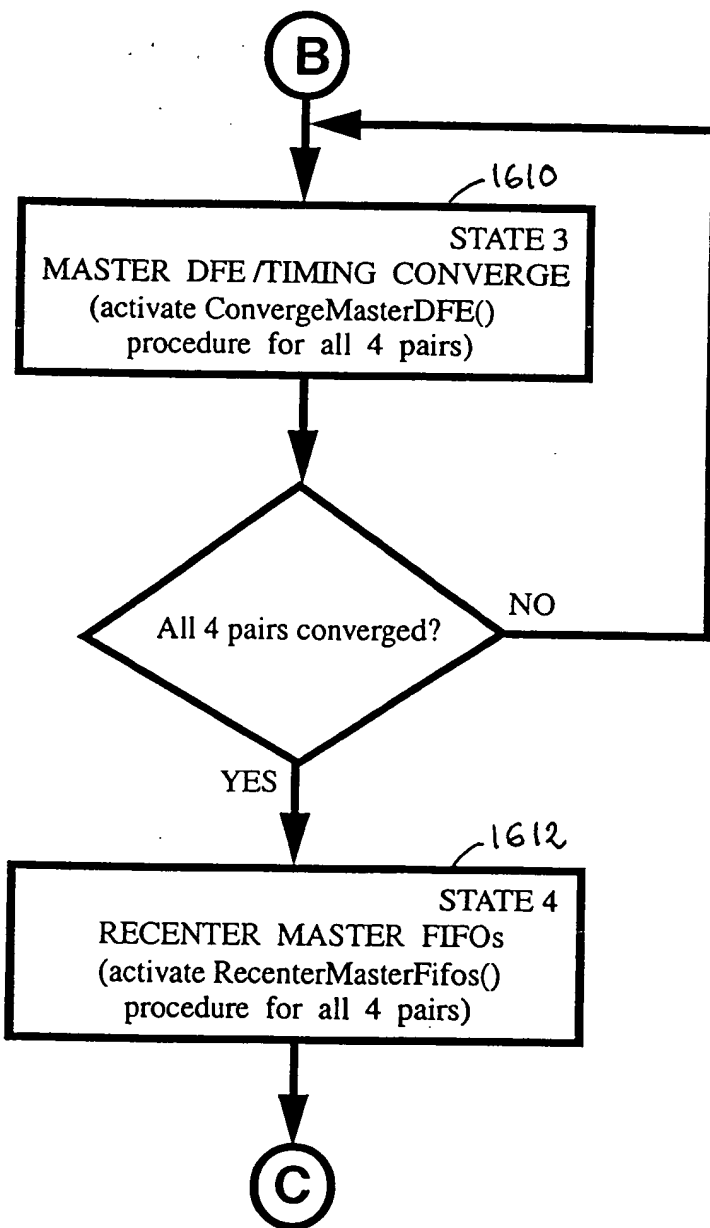


FIG. 16 C

004240" 4225560

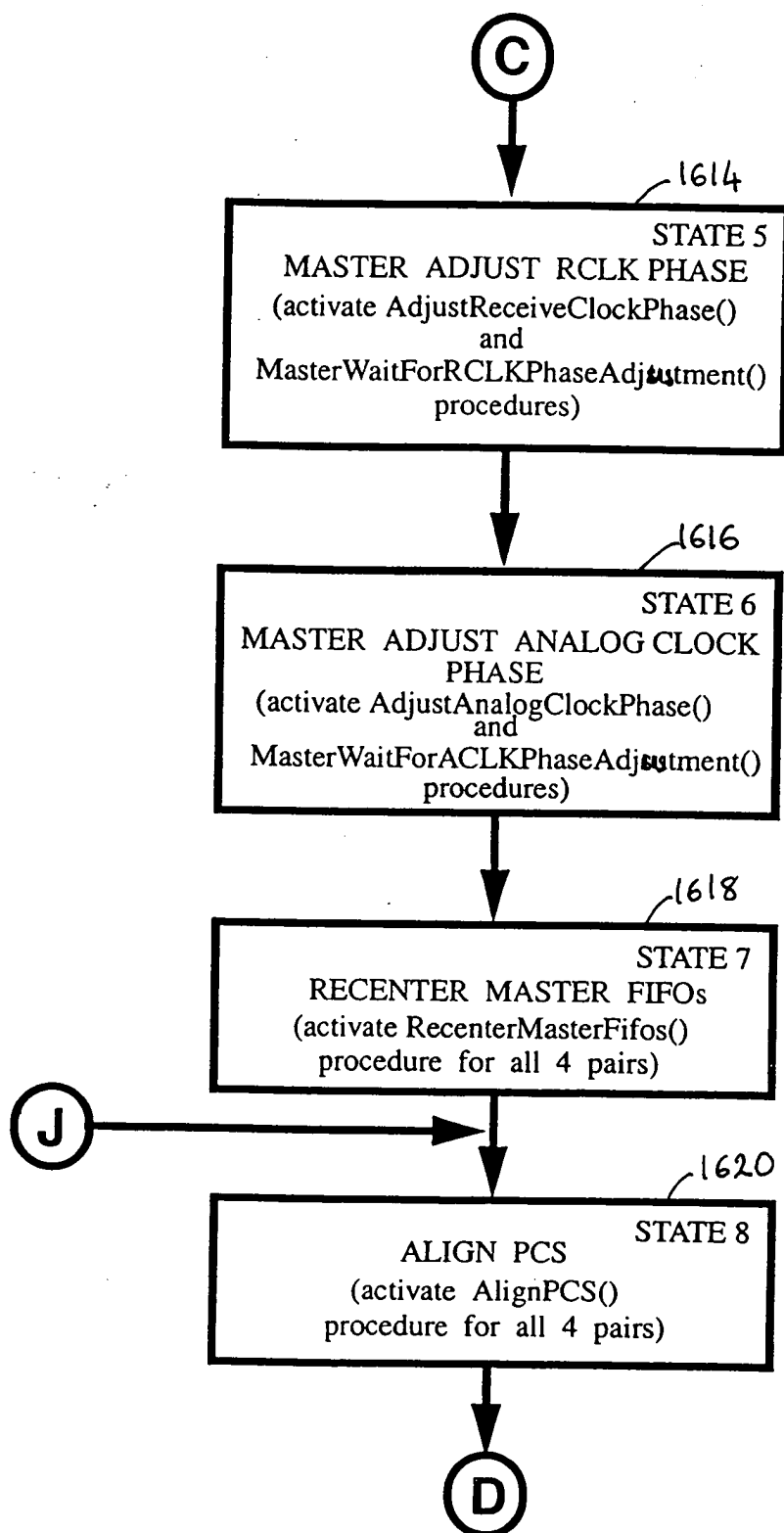


FIG. 16 D

001240" 422550

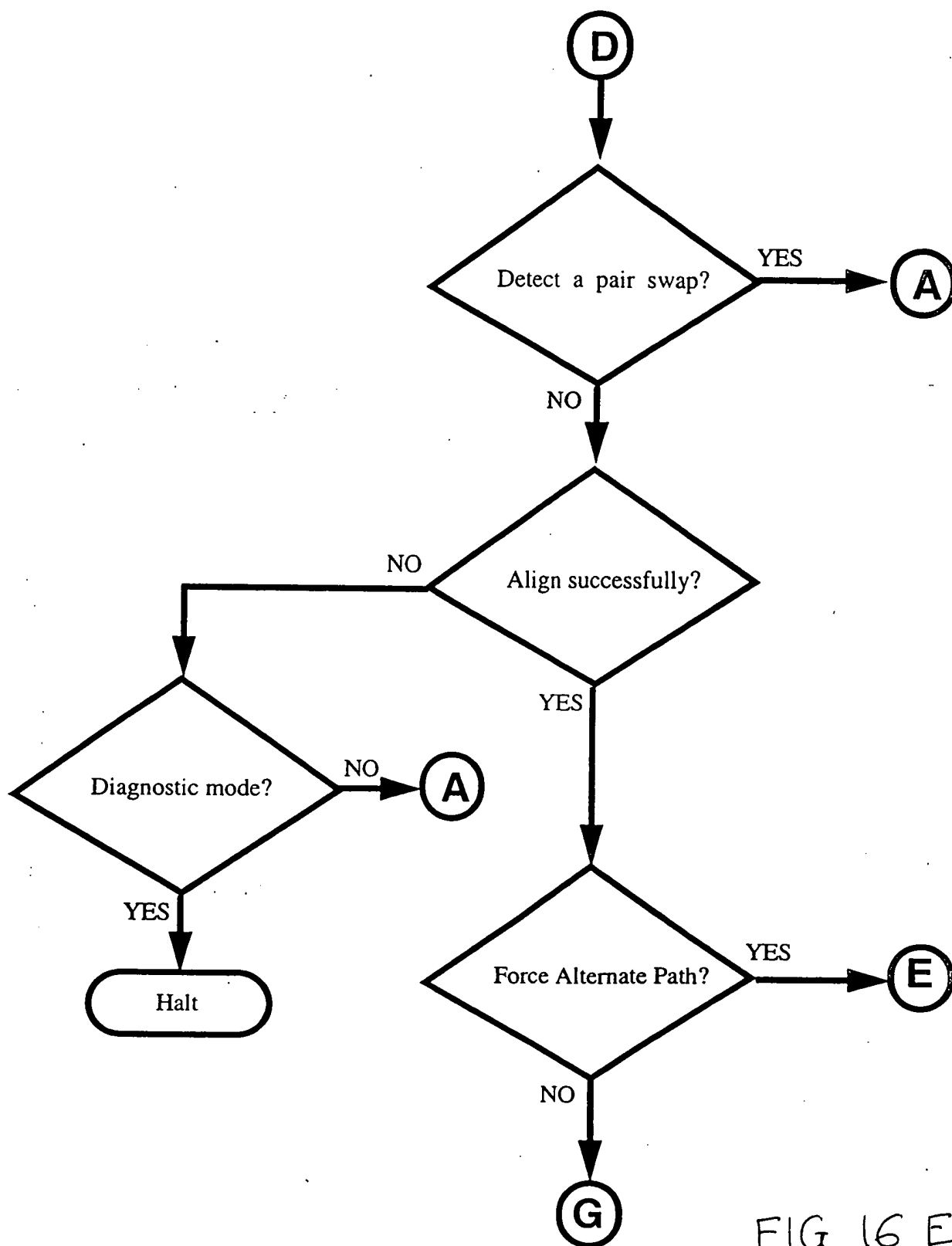


FIG 16 E

004240-422560

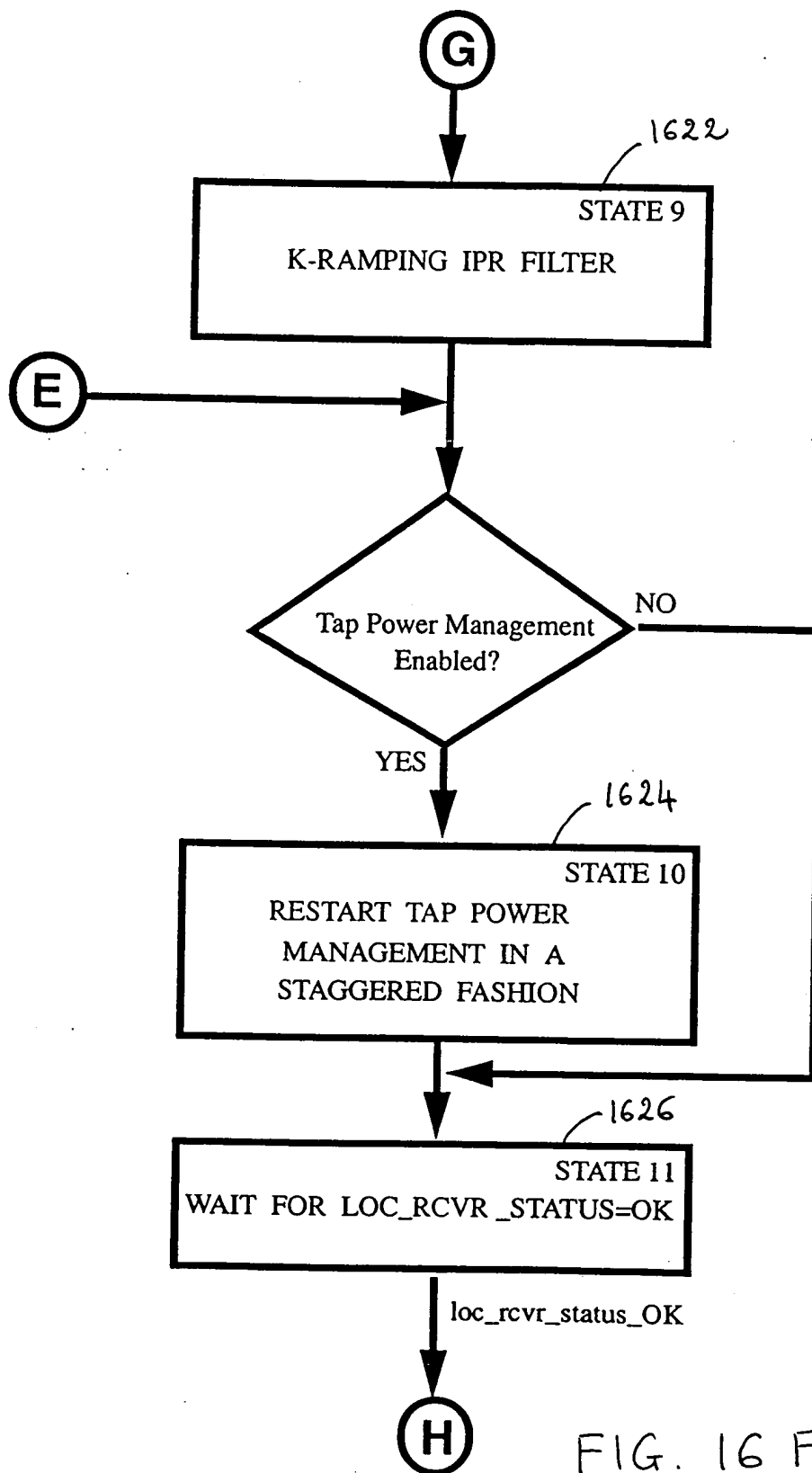


FIG. 16 F

004240"422550

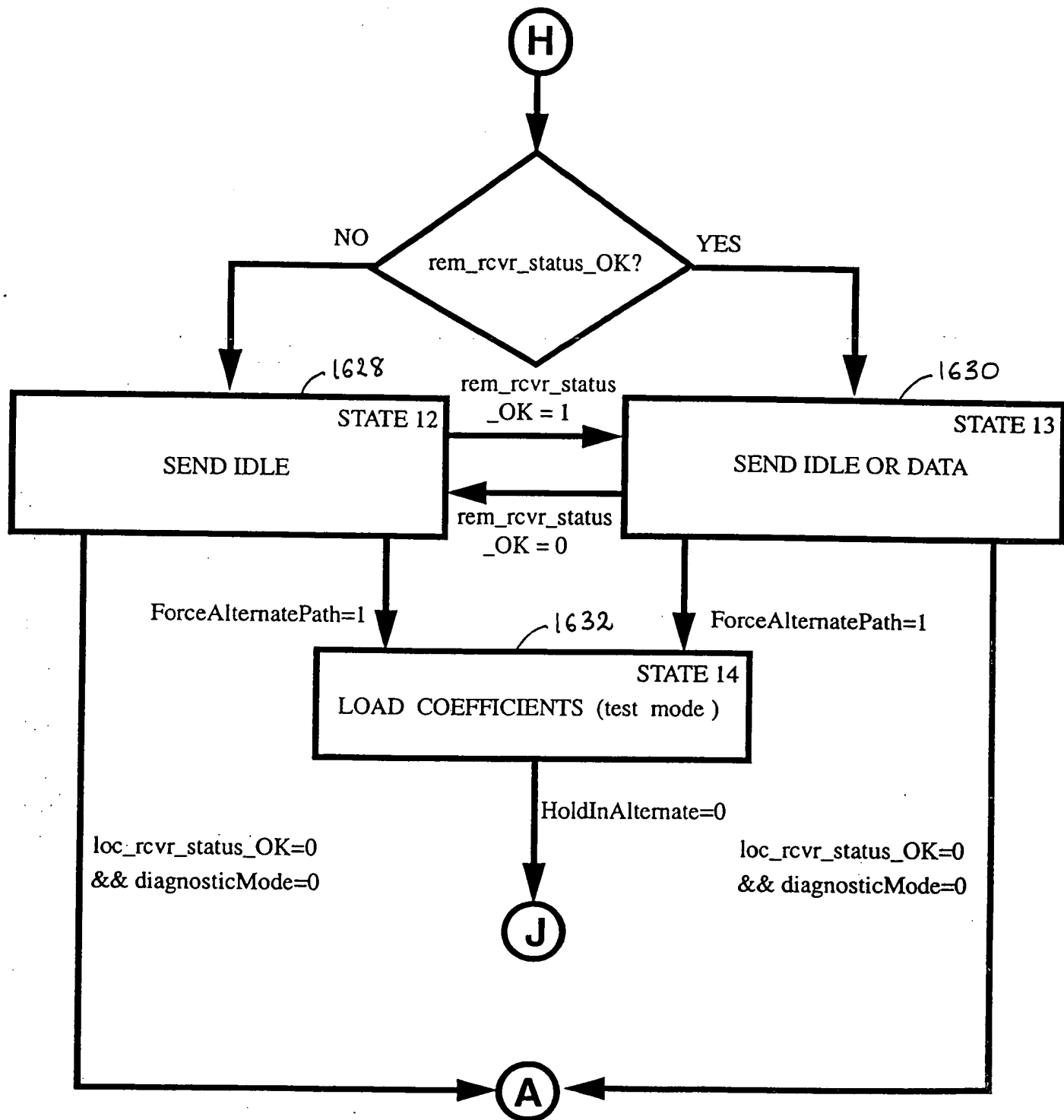


FIG. 16G

004240"422550

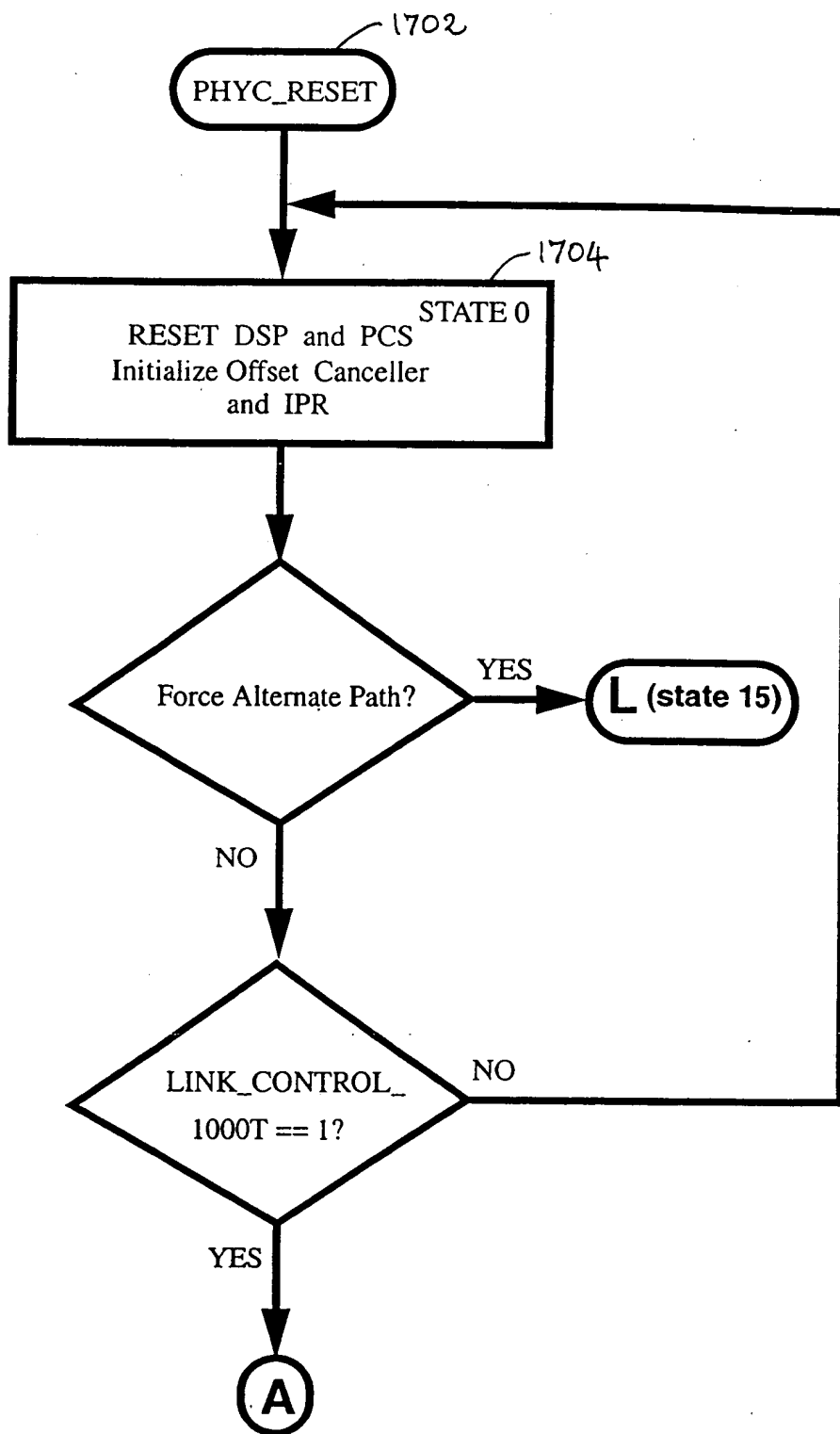


FIG. 17A

004240-422560

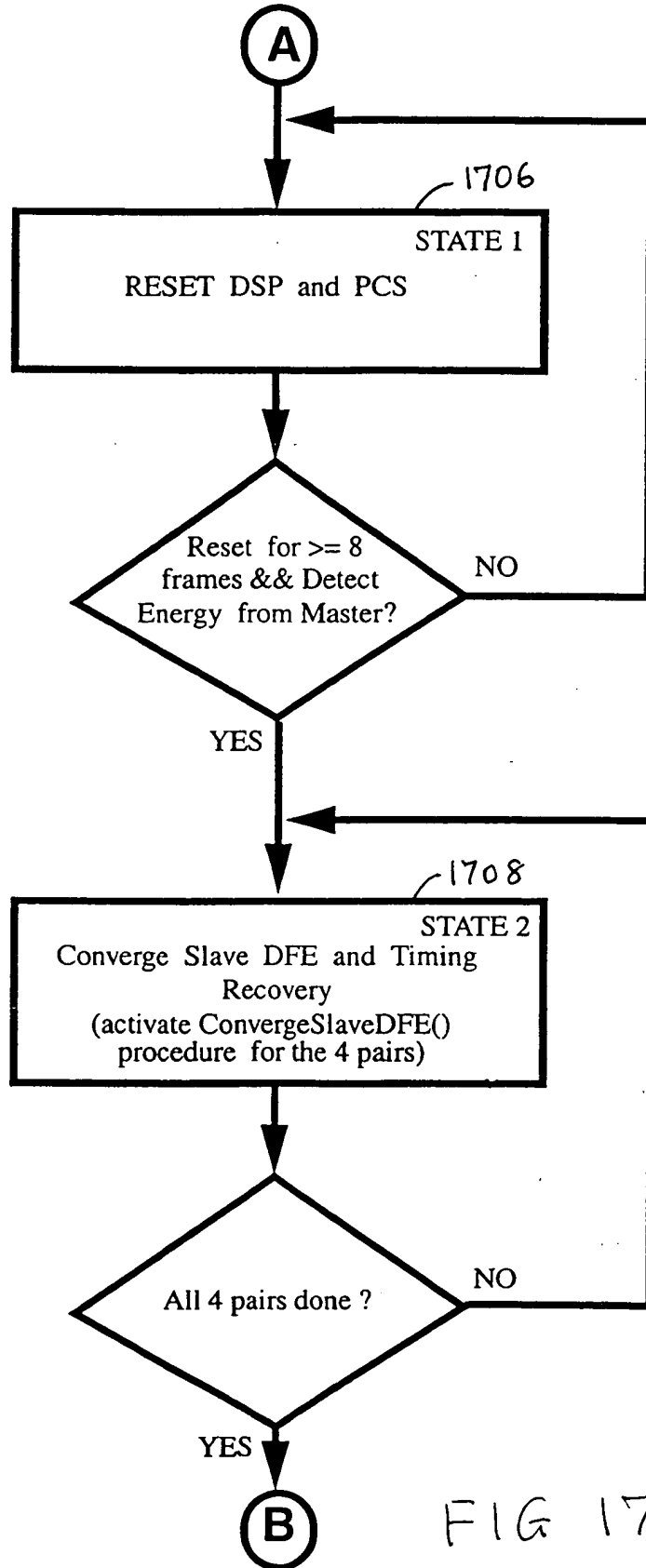


FIG 17B

005724.0440 004240-7225560

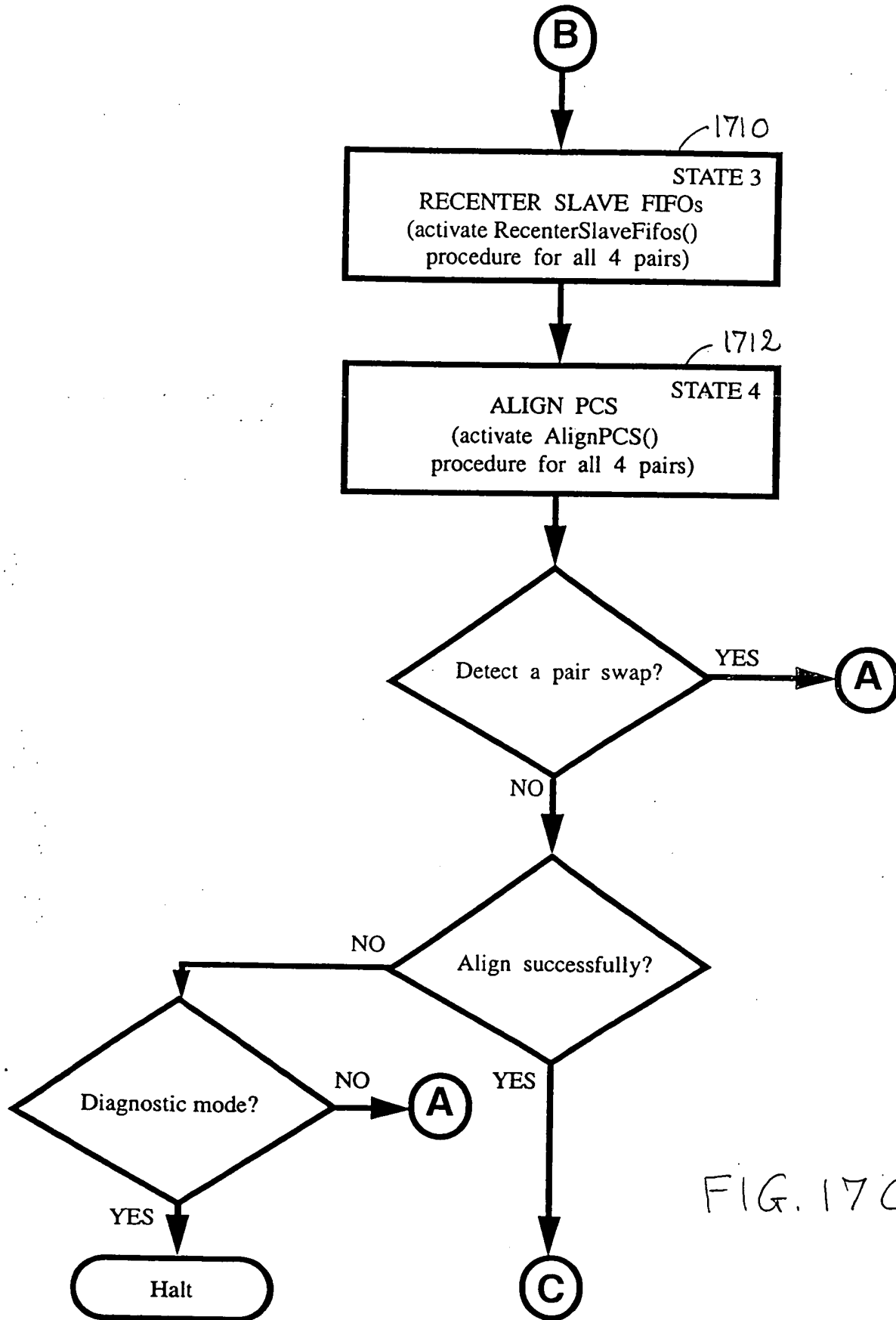


FIG. 17C

004440" 422550

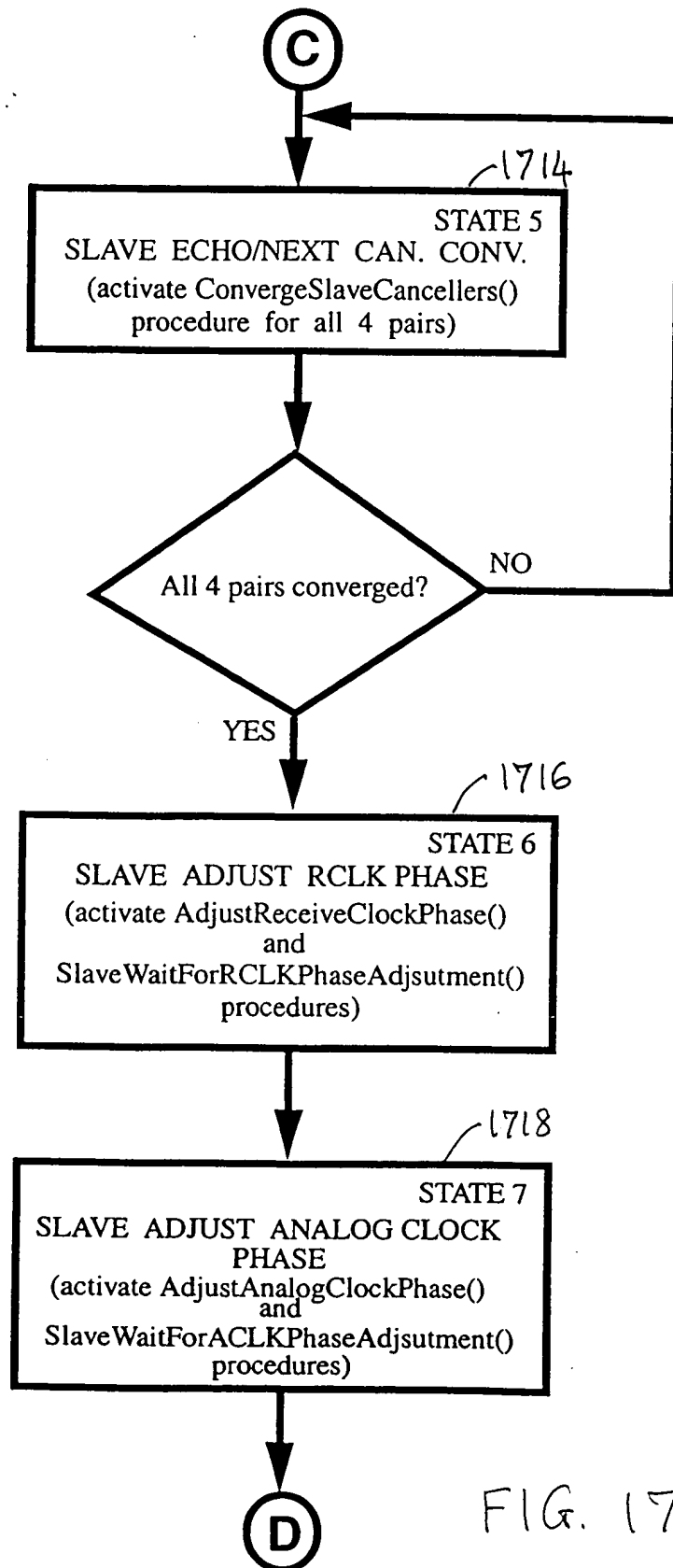


FIG. 17D

004240-4225560

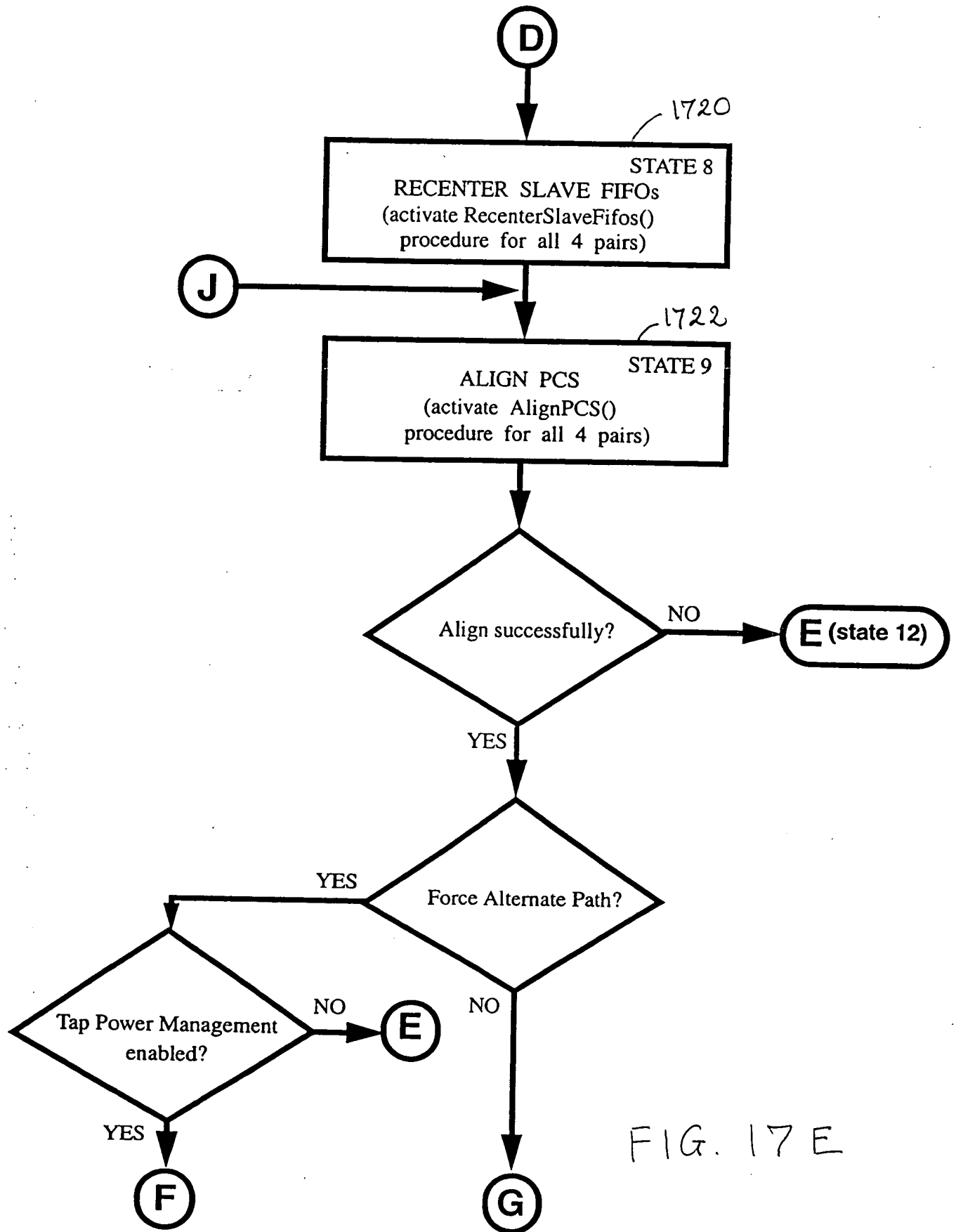


FIG. 17E

004240-422550

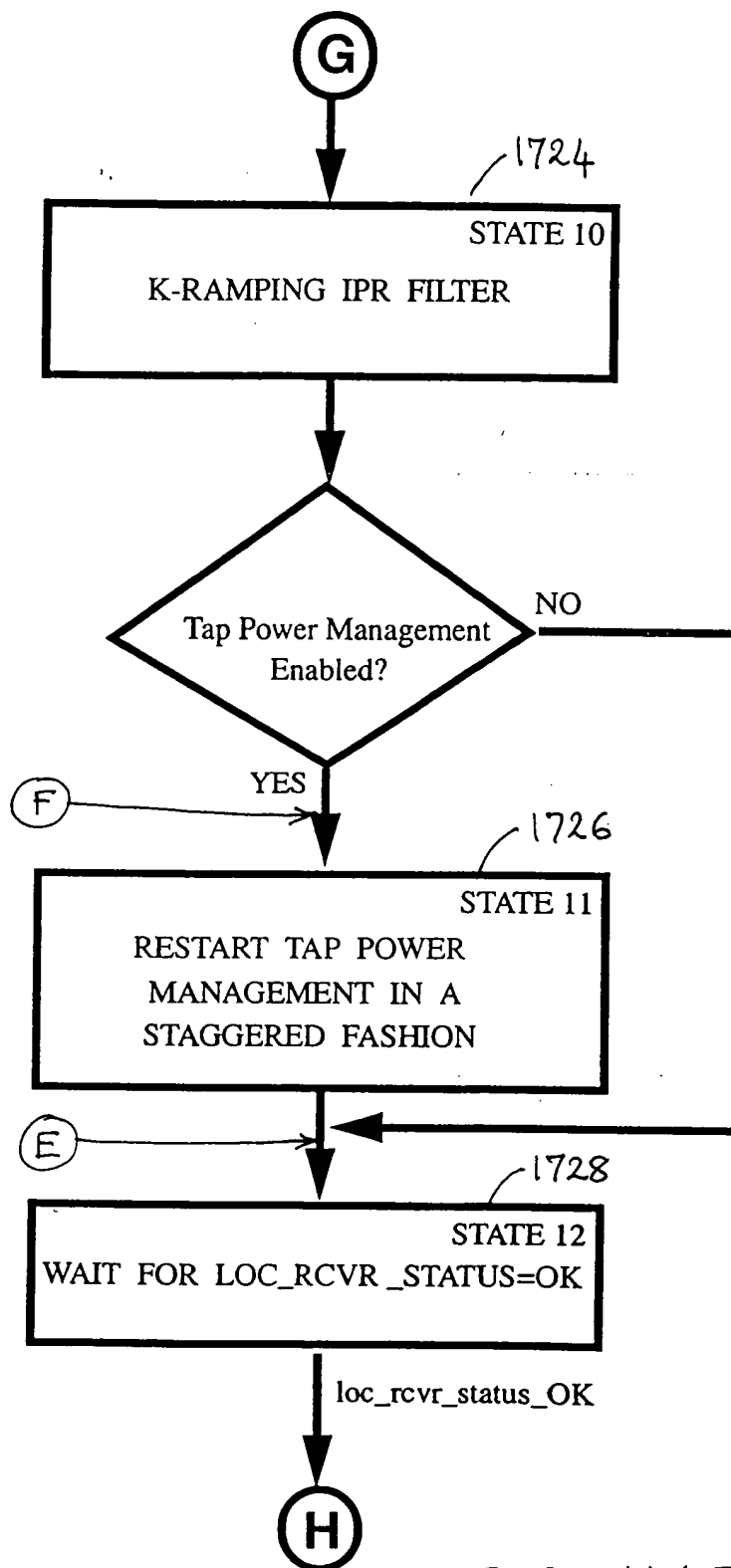


FIG. 17F

00440-422560

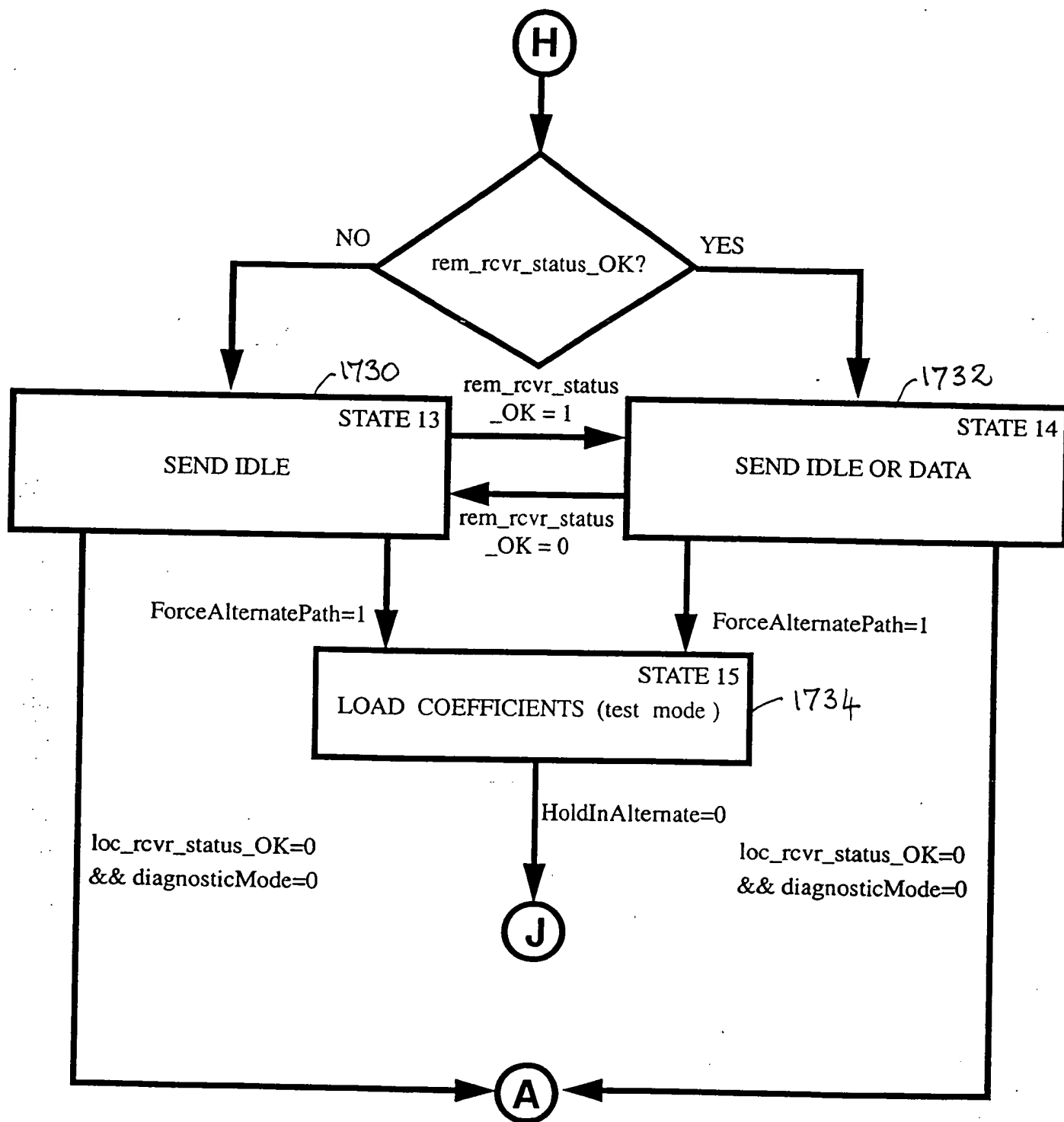


FIG. 17 G

004240-42560

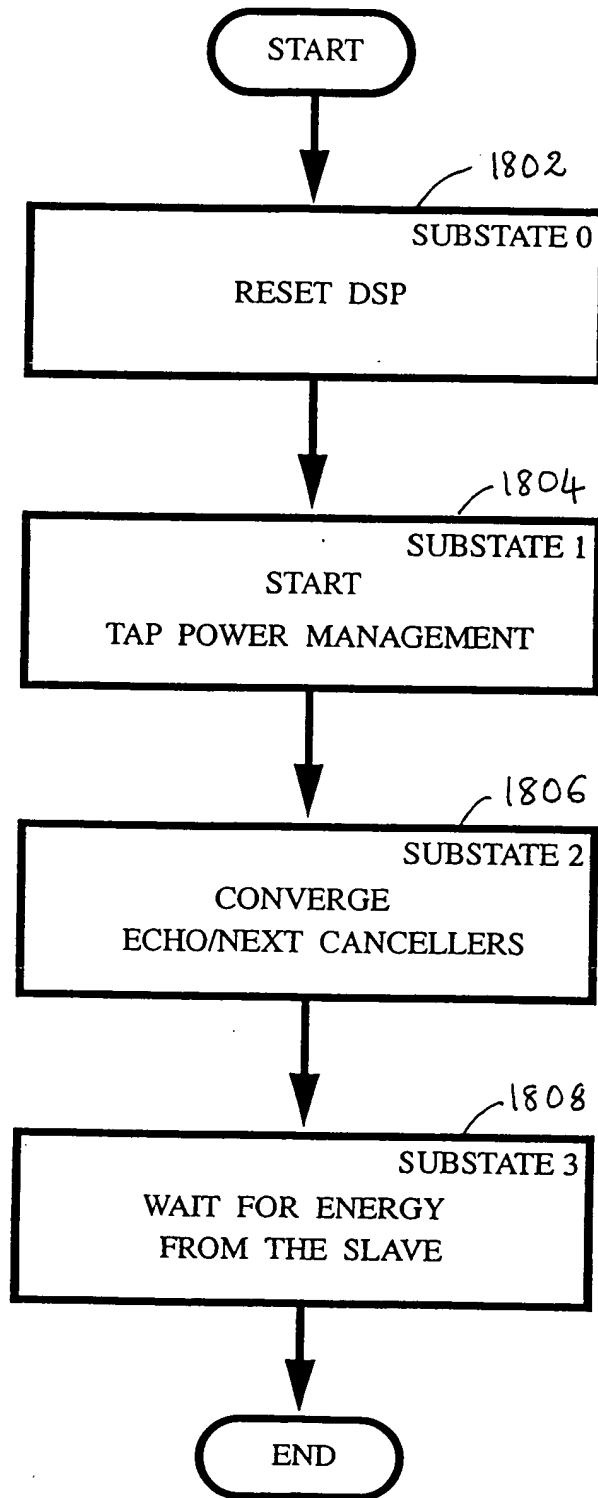


FIG. 18

004270" 4/2/5560

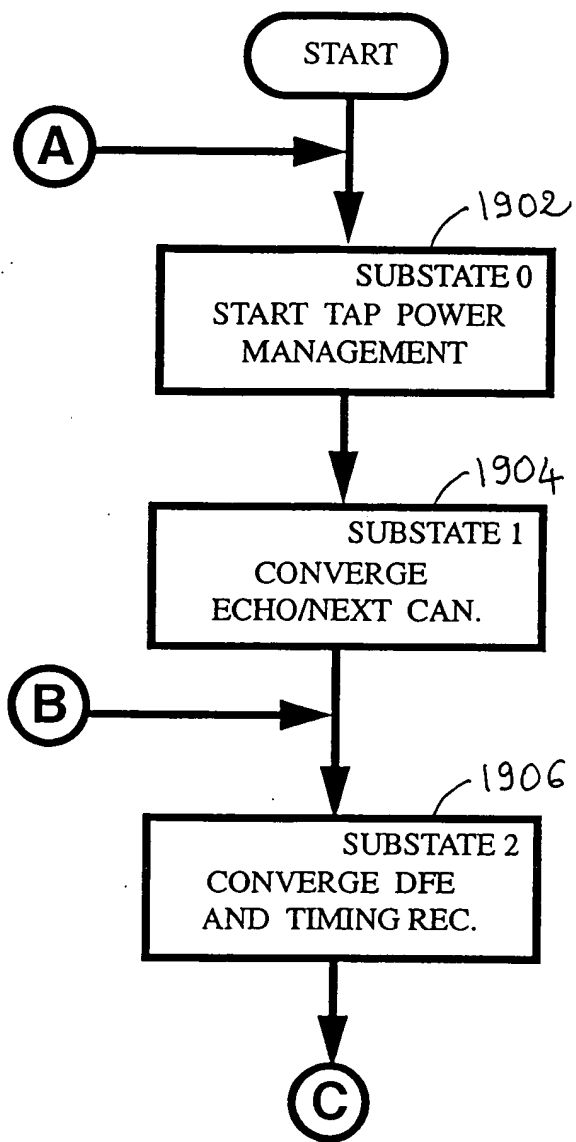


FIG. 19A

004240" 12.5560

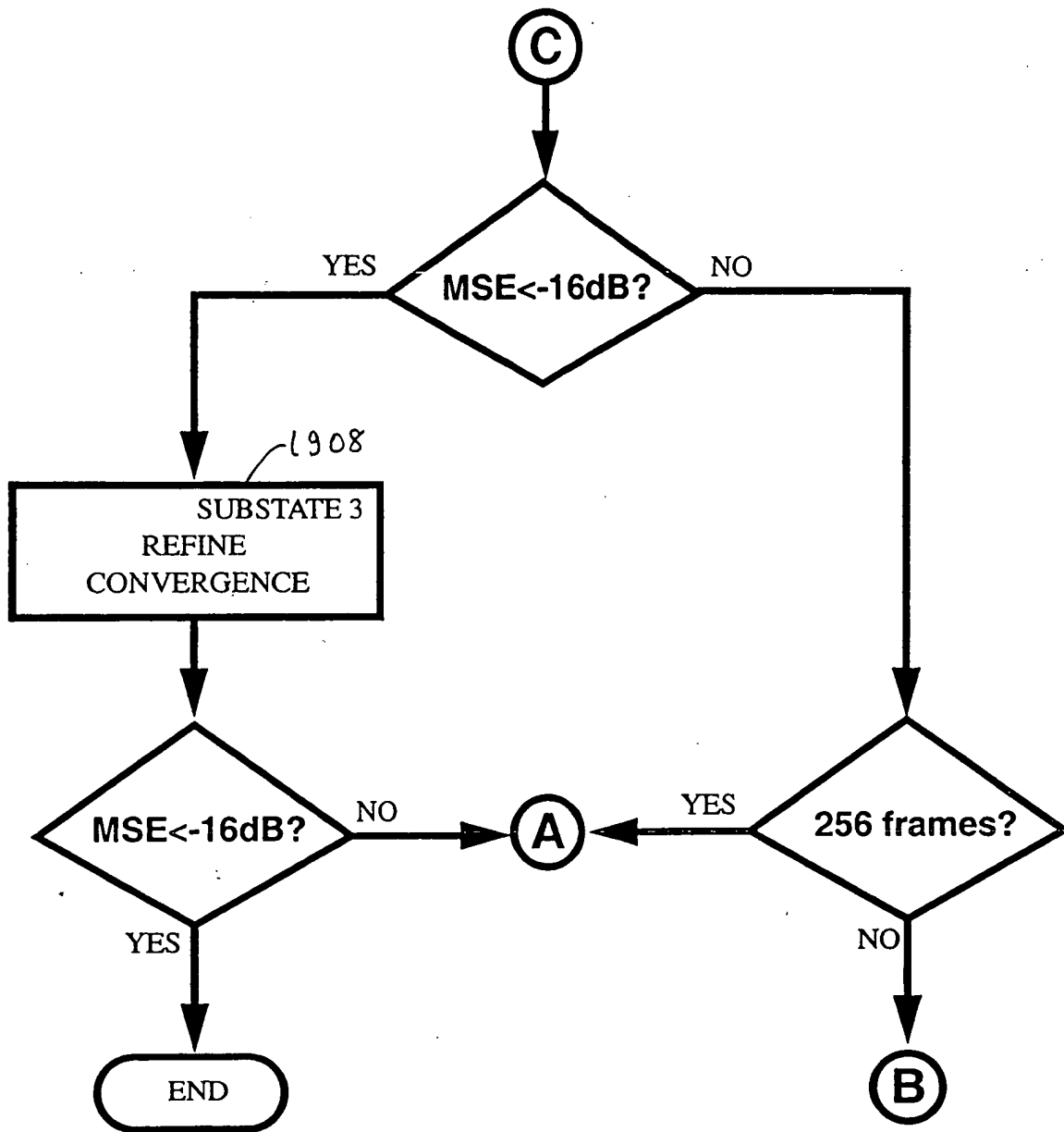


FIG. 19B

004240" 4225560

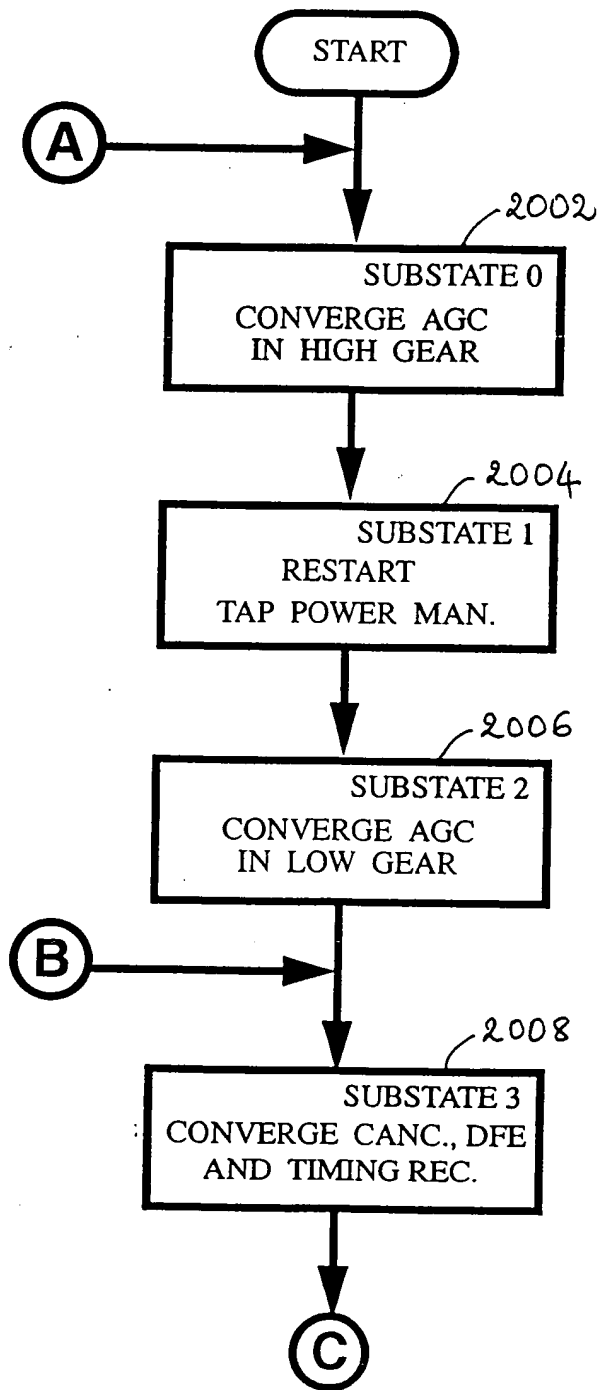


FIG. 20A

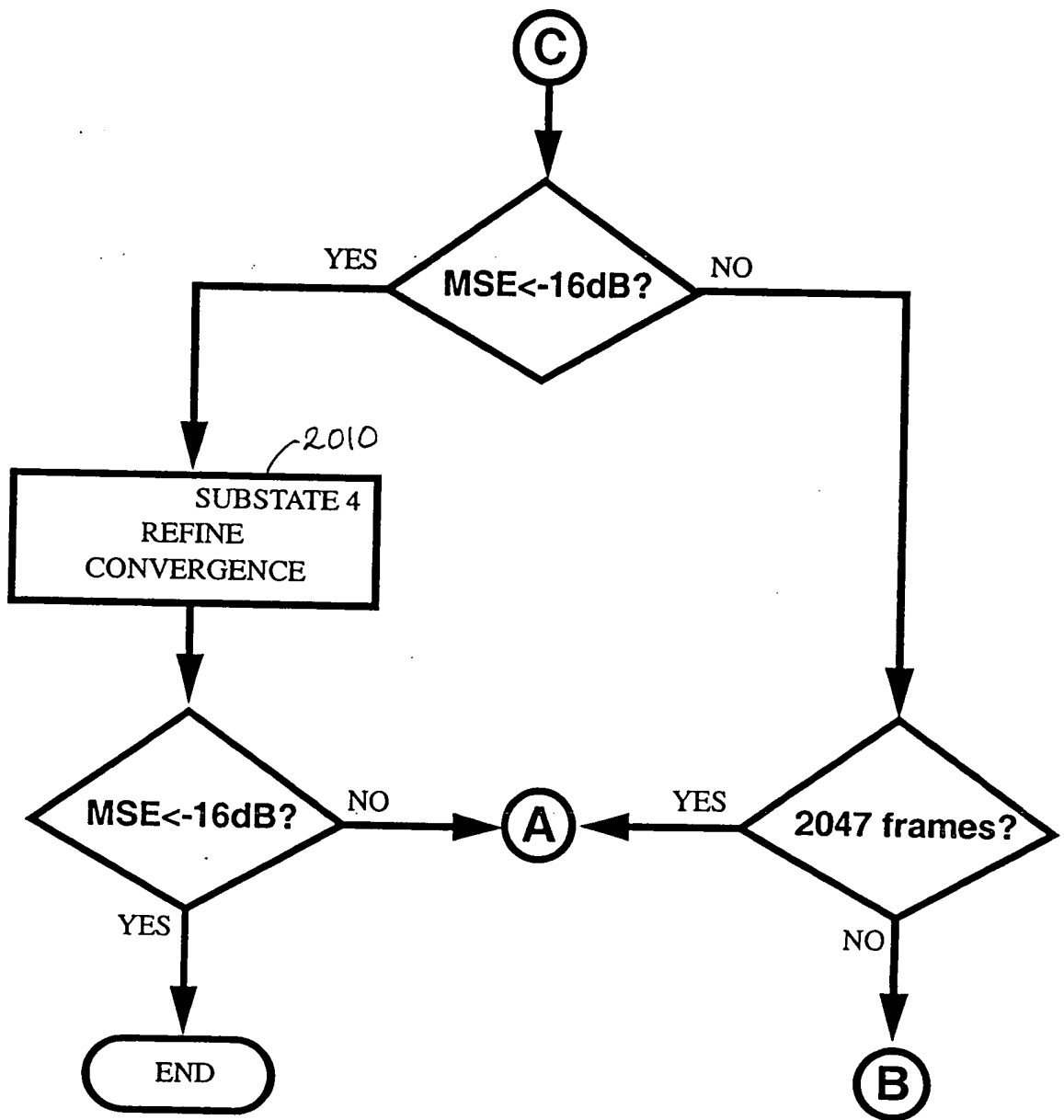


FIG. 20B

004240" 4225550

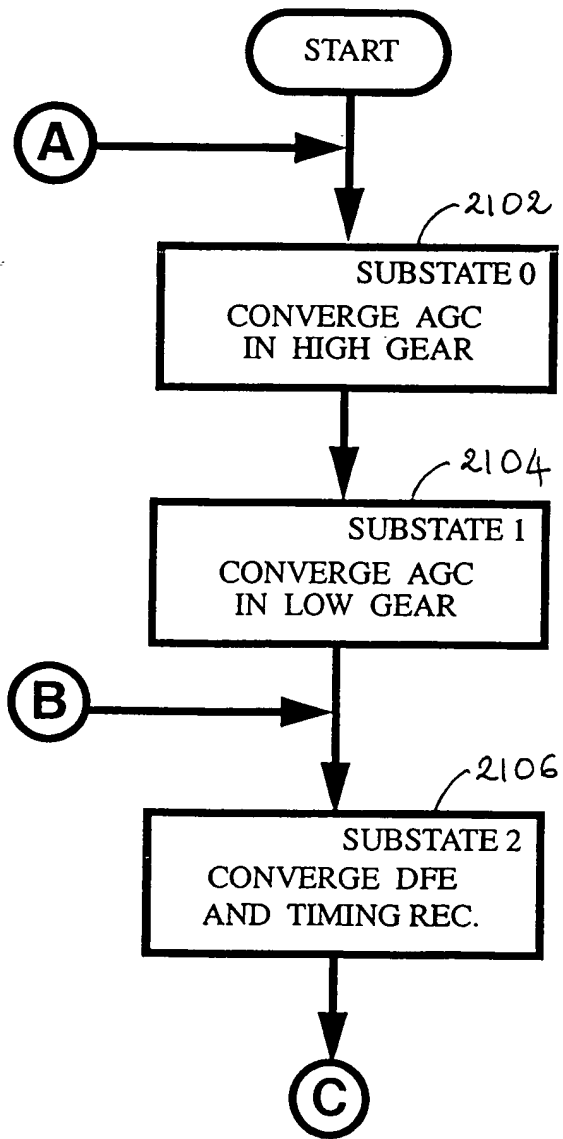


FIG. 21 A

004240" 4225560

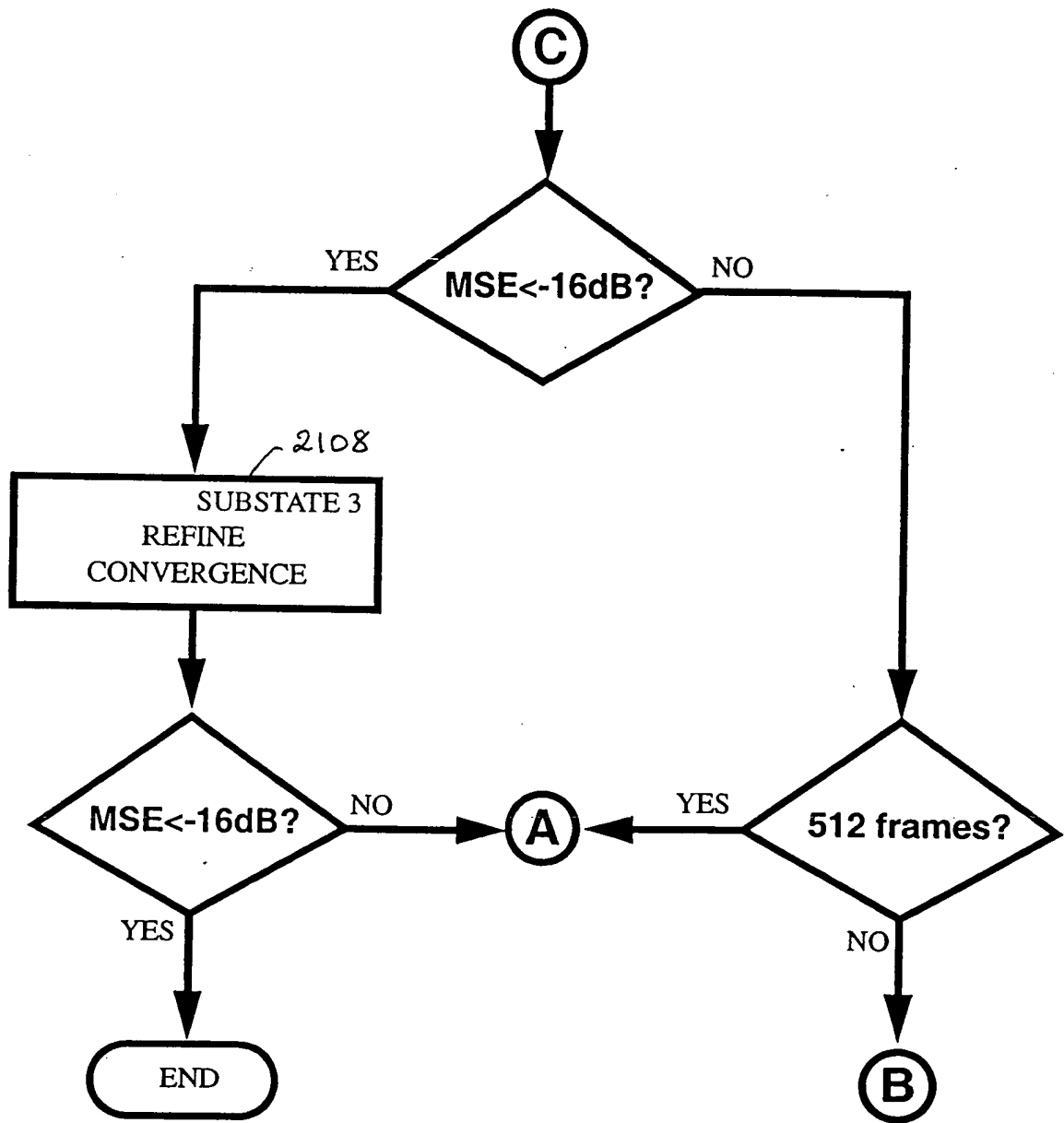


FIG. 21B

004240" 4245560

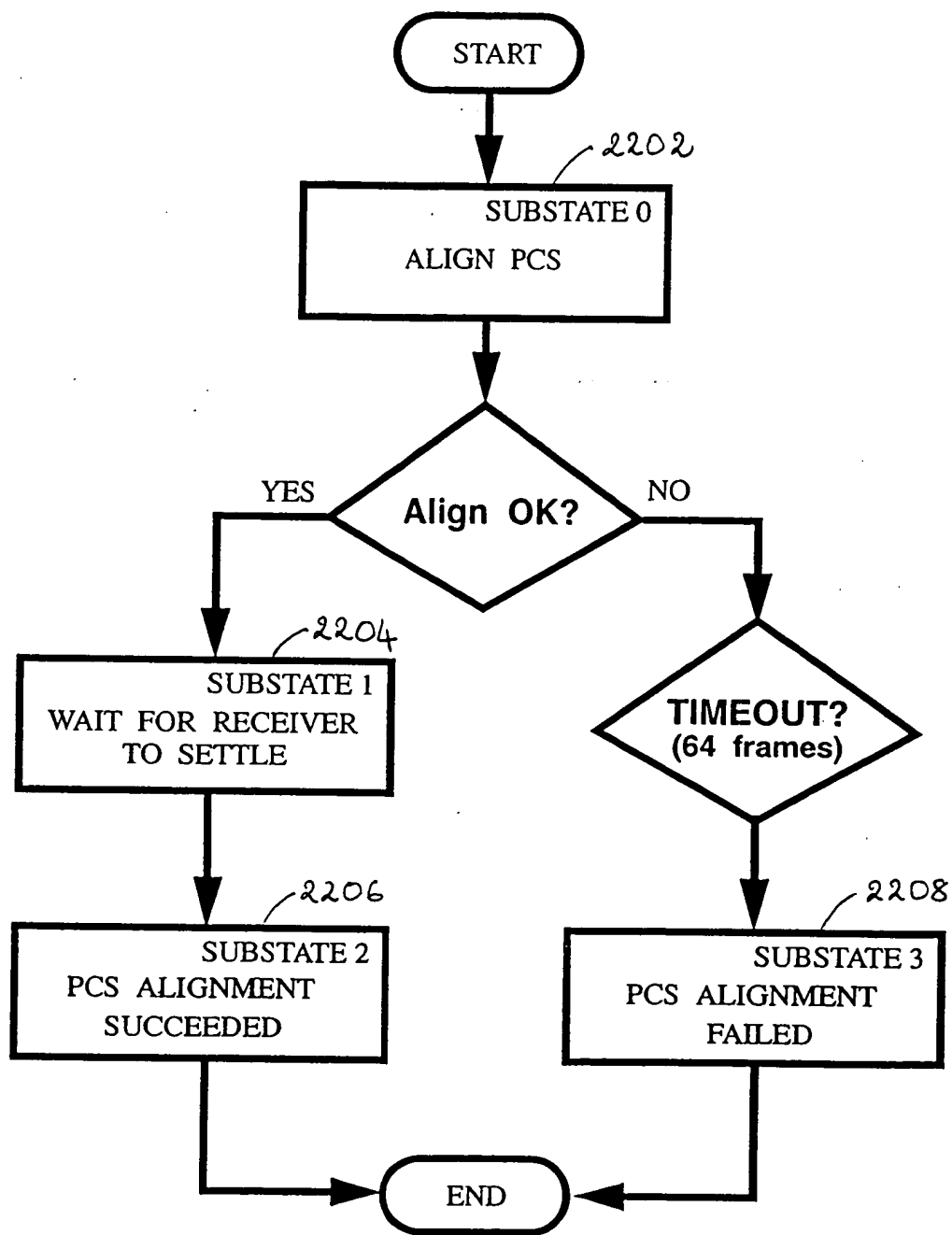


FIG. 22

004240" 4225560

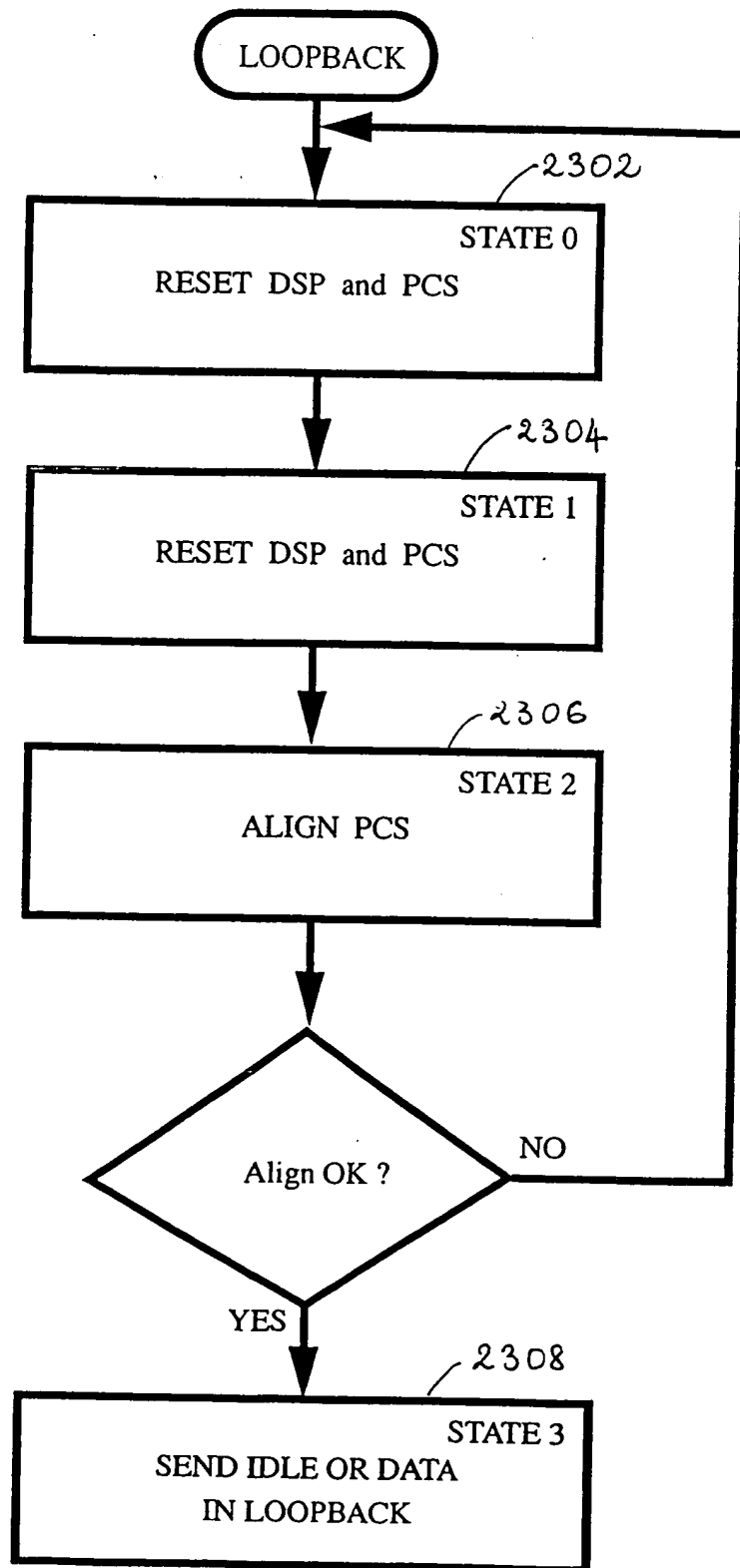


FIG. 23

004240" 4245560

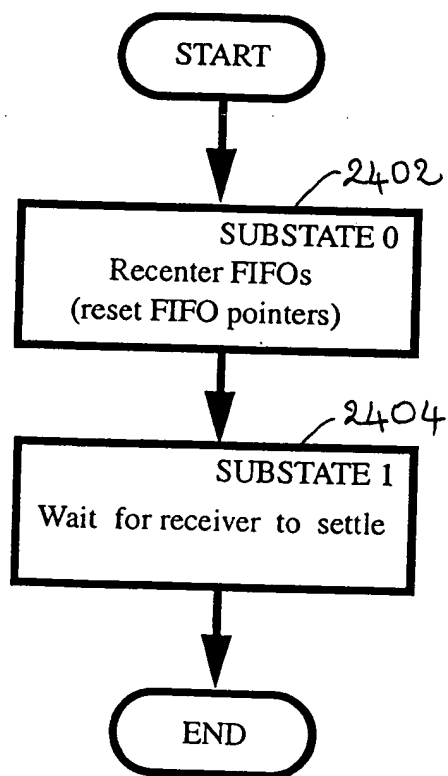
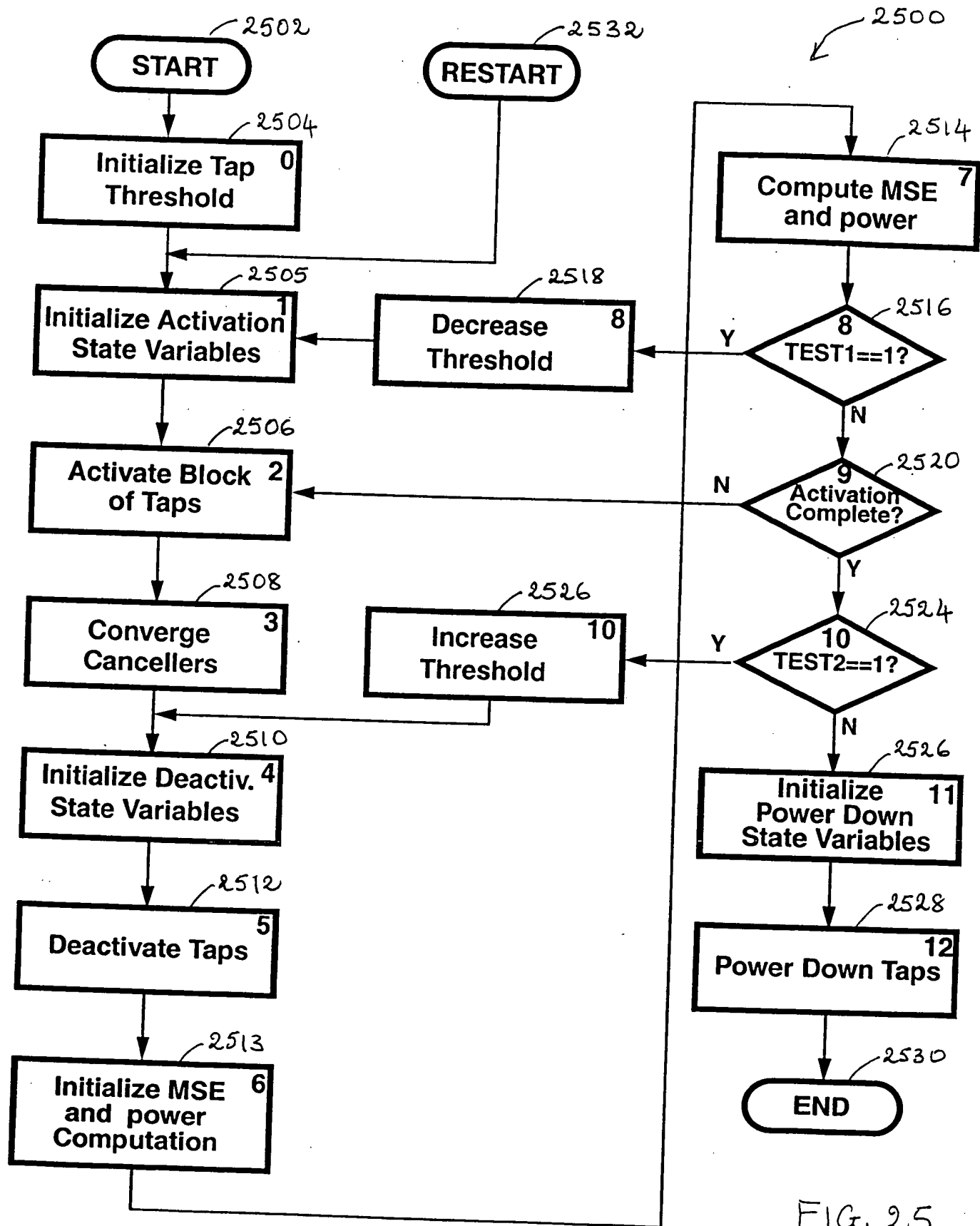


FIG. 24

004240" 4225560



TEST1 = (MSE>Target) && (power<MaxPower)
TEST2 = (MSE<Target) || (power>MaxPower)

FIG. 25

004240" 423550

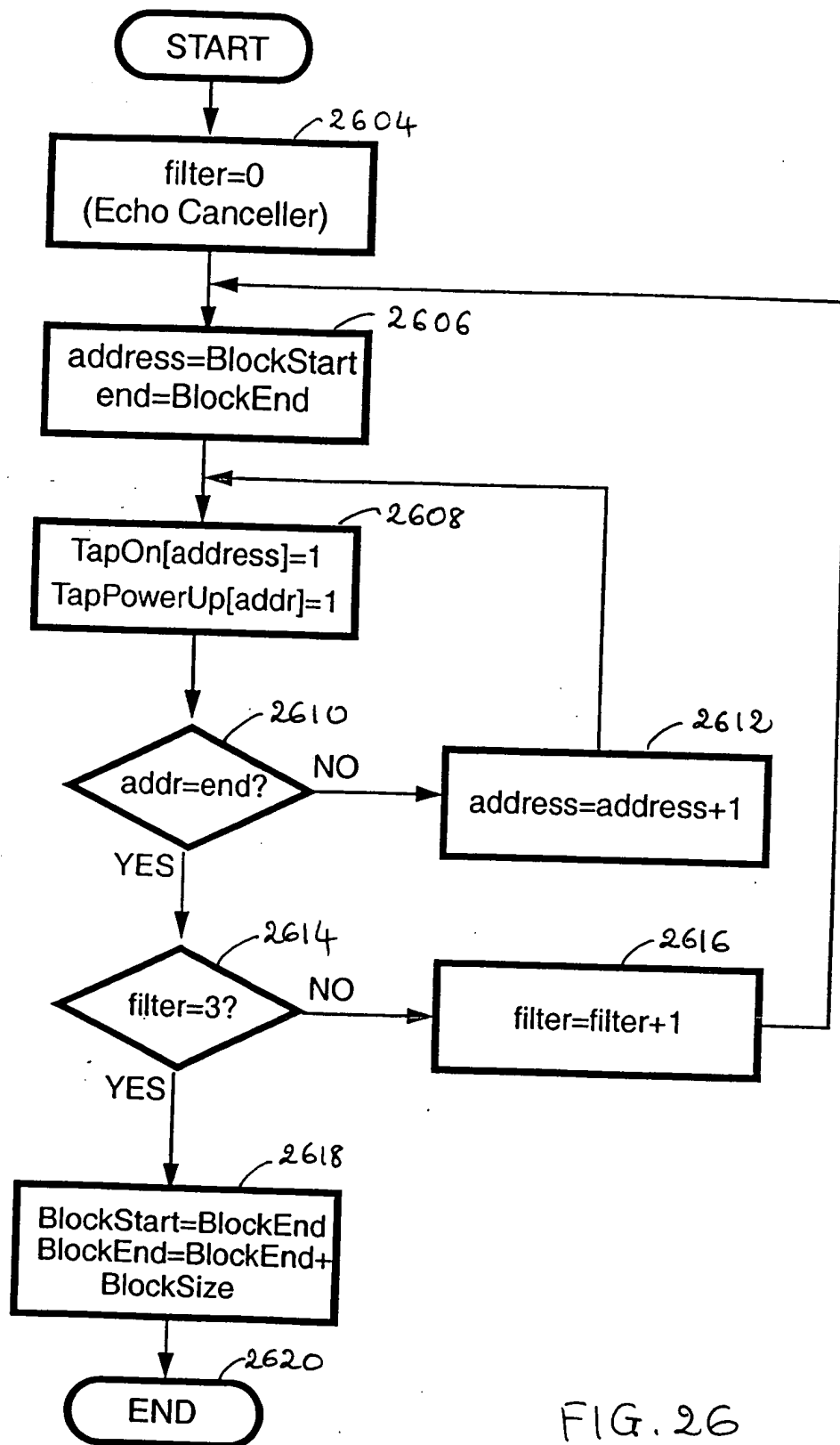


FIG. 26

004240" 422560

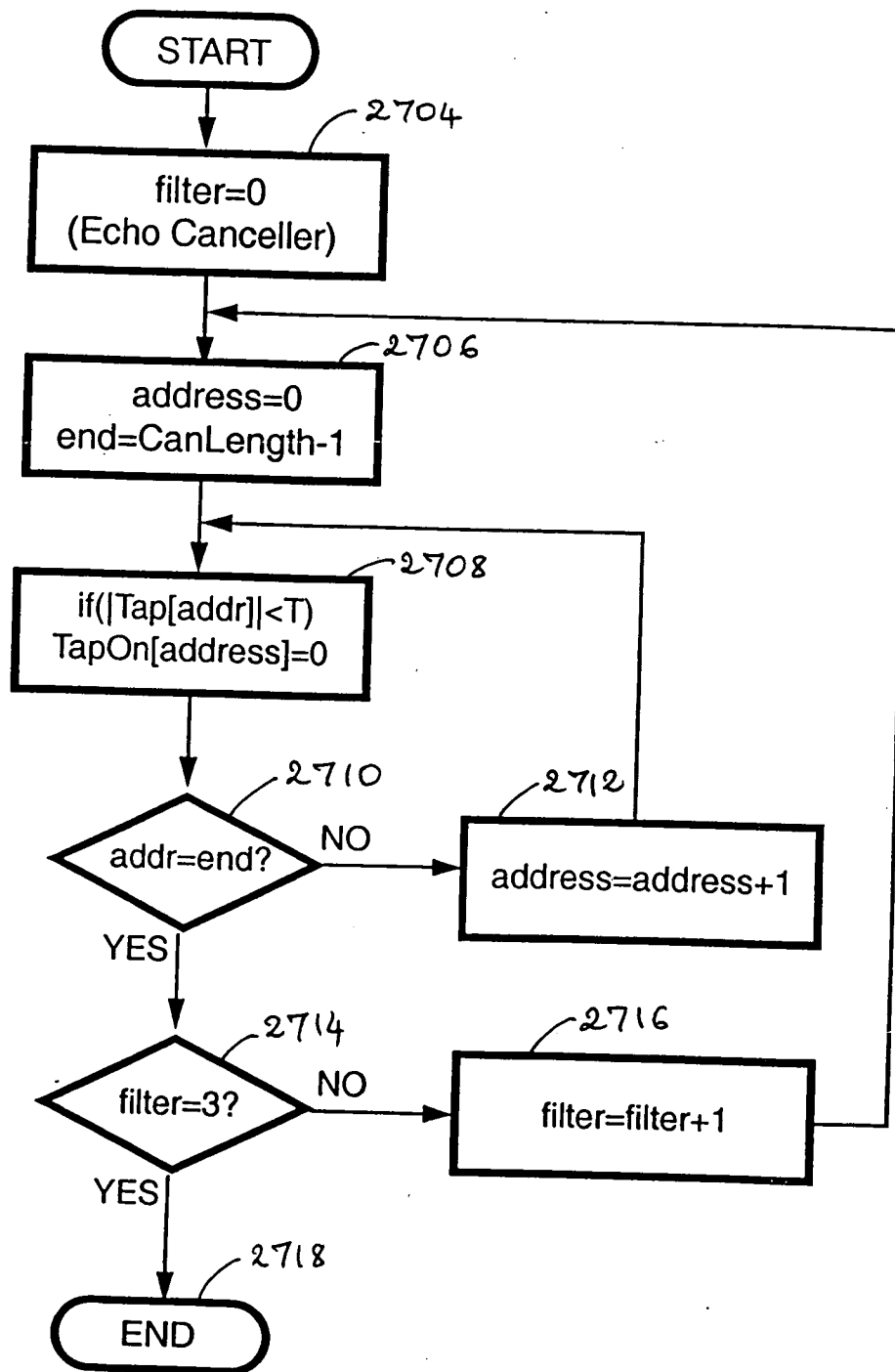


FIG. 27

004240" 424560

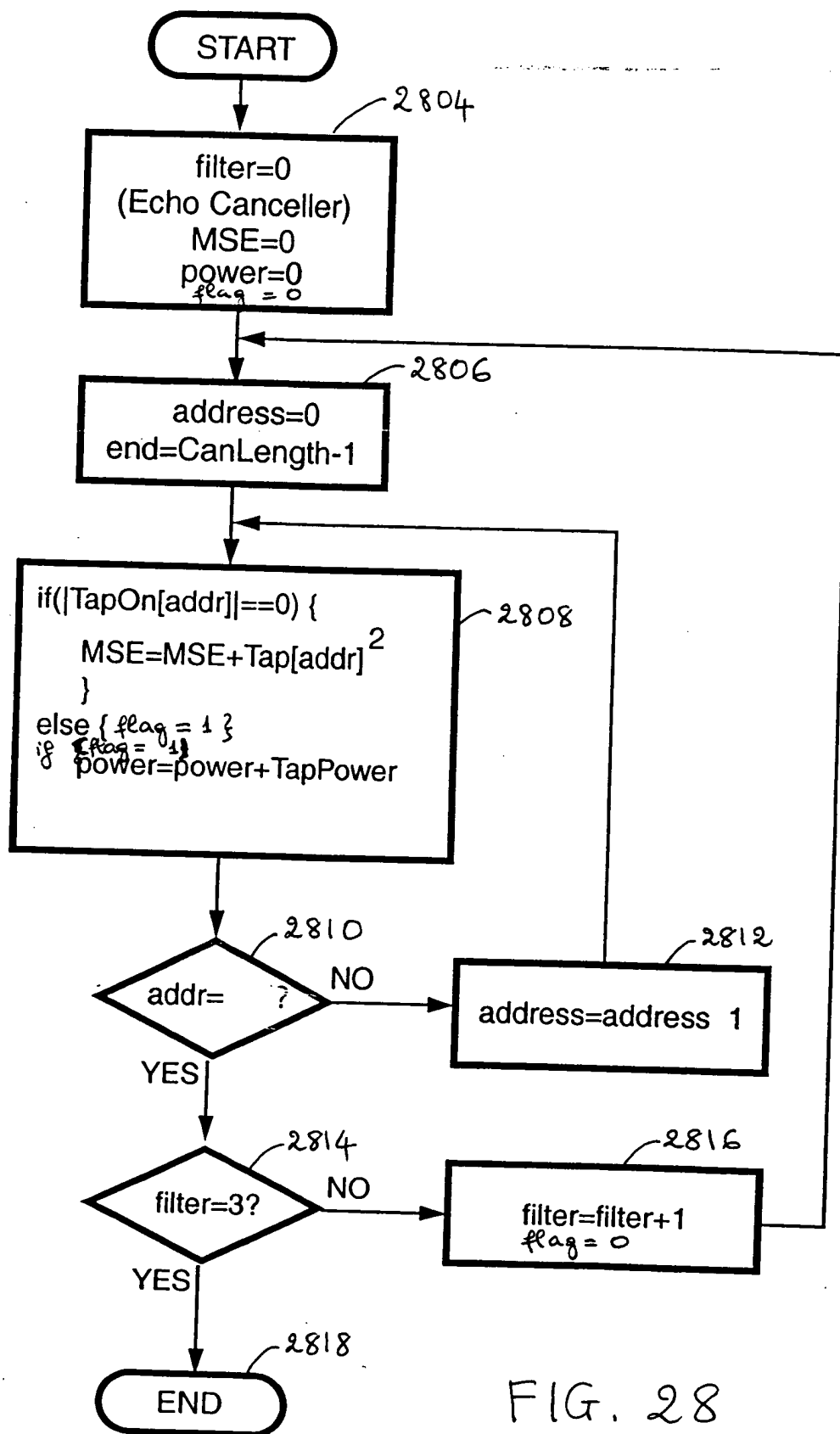


FIG. 28

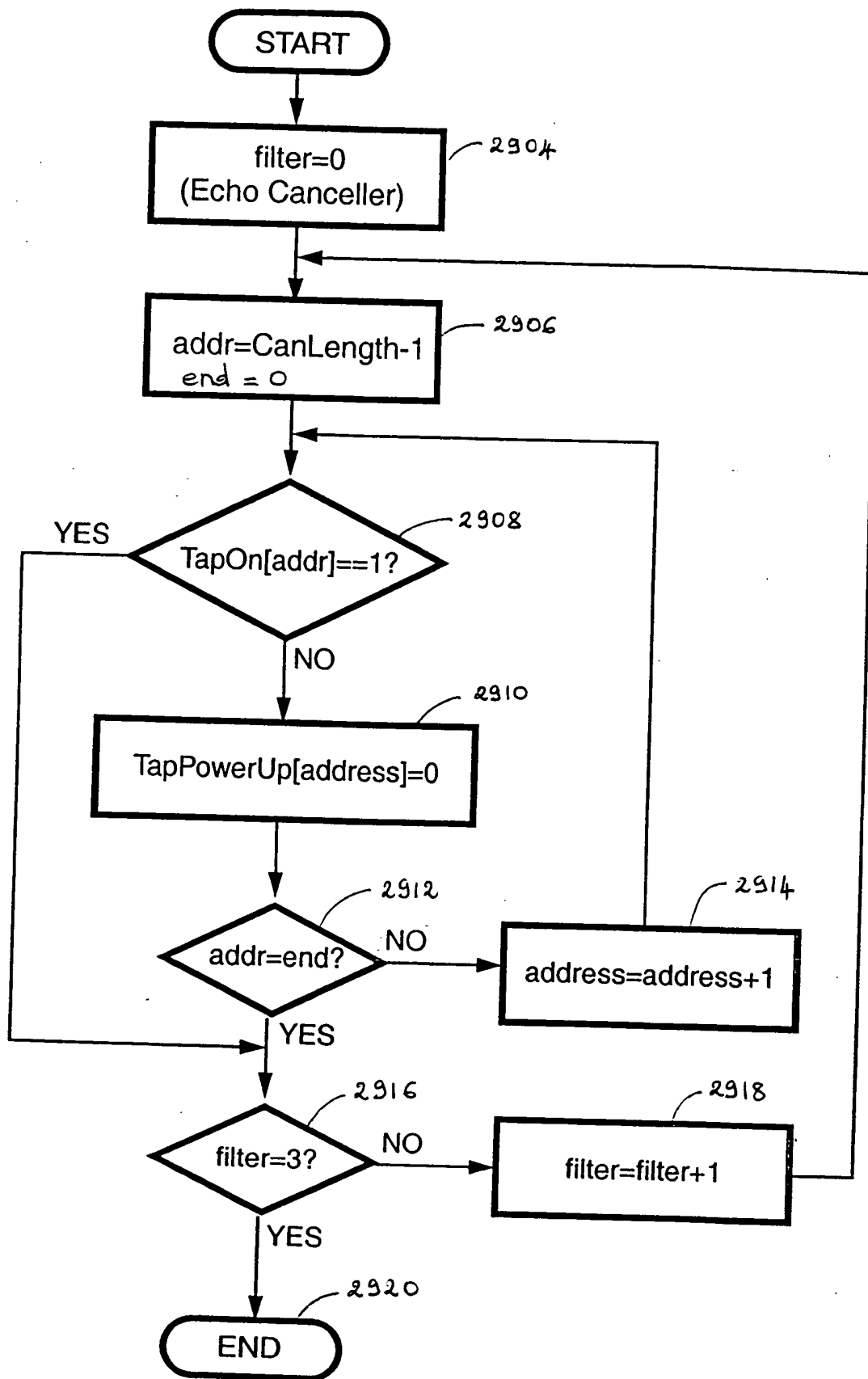


FIG. 29

004240" 4225560